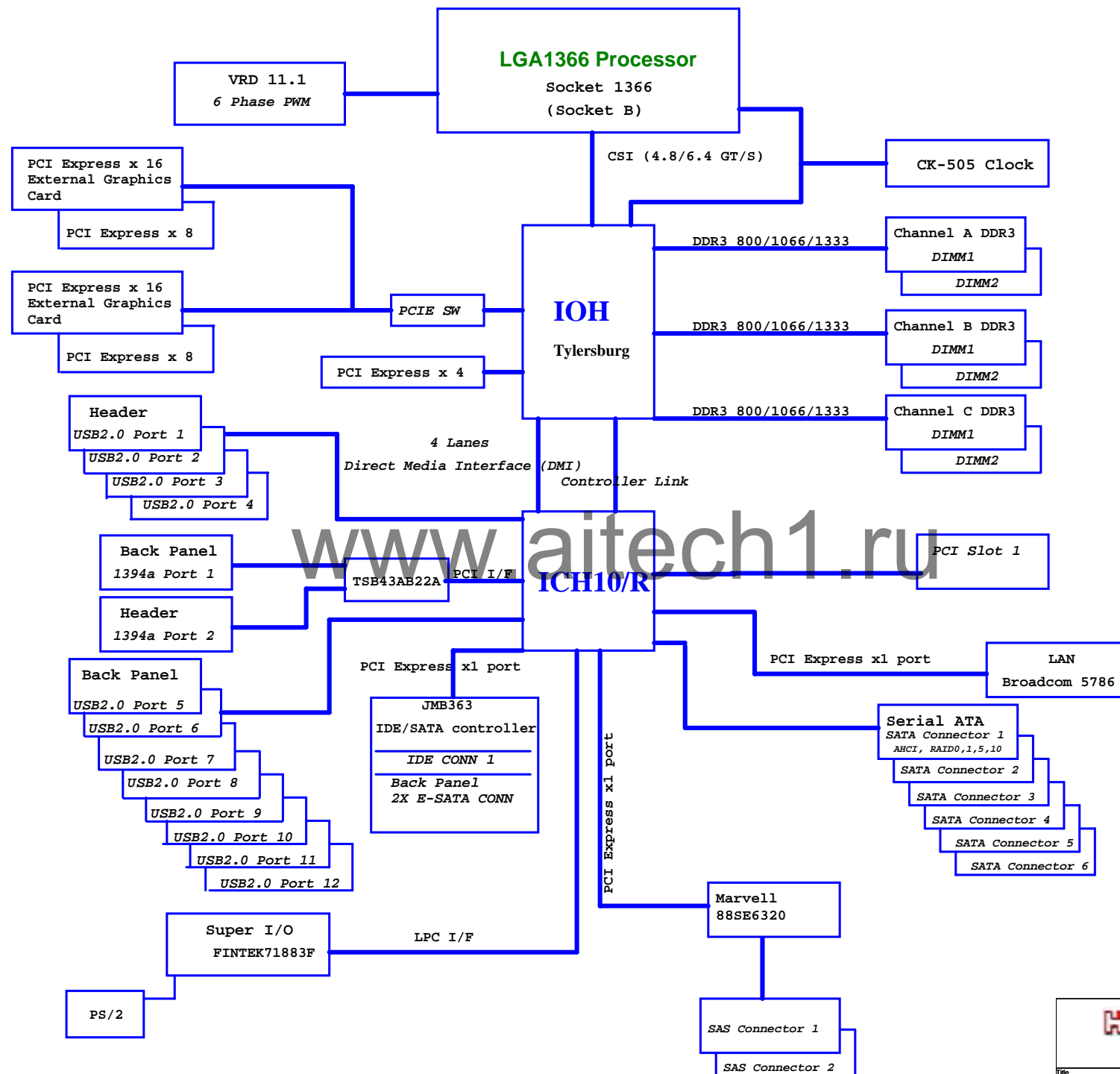


Foxconn Precision Co. Inc.

TBGA01 Schematic

Page Index

01. Index Page	28. PCI Slot
02. Topology	29. SIO FINTEK F71883
03. Clock Distribution	30. HDA ALC888S
04. Power Delivery Map	31. JMICRON JMB363
05. CK505 CLK GEN	32. LAN Broadcom BCM5786
06. VRD 11.1	33. LAN1 Broadcom BCM5786
07. Socket 1366 -1	34. 1394 TSB43AB22A
08. Socket 1366 -2	35. Power / MISC Connectors
09. Socket 1366 -3	36. CPU/System Fan
10. Socket 1366 -4	37. Serial FLASH
11. Tylersburg IOH -1	38. KB/MS, Floopy
12. Tylersburg IOH -2	39. HDA Connector
13. Tylersburg IOH -3	40. 1394/ESATA Connector
14. Tylersburg IOH -4	41. Front USB Connector
15. Tylersburg IOH -5	42. LAN/USB Connector
16. XDP	43. FOX-DOLL
17. DDR3 Channel A DIMM 1,2	44. POWER-1
18. DDR3 Channel B DIMM 1,2	45. POWER-2
19. DDR3 Channel C DIMM 1,2	46. POWER-3
20. ICH10-1	47. POWER-4
21. ICH10-2	48. EMC
22. ICH10-3	49. GPIO/IRQ/IDSEL MAP
23. ICH10-4	50. History
24. PCIE X16_SLOT 0,1	
25. PCIE X16_SLOT 2,3	
26. PCIE X16_SW	
27. PCIE X4_SLOT	



14.318MHz

CPU

CPU 200/266/333 MHz Diff Pair

MCH 200/266/333 MHz Diff Pair

PCI Express 100 MHz Diff Pair

PCI Express x16 Gfx

IOH

Tylersburg

DDR3 6 Slots
DDR3 800/1066/1333

Channel A DDR3

DIMM1

DIMM2

Channel B DDR3

DIMM1

DIMM2

Channel C DDR3

DIMM1

DIMM2

PCI Express/DMI 100 MHz Diff Pair

PCI Express/DMI 100 MHz Diff Pair

USB/SIO 48 MHz

ICH 33 MHz

REF 14 MHz

ICH10R

PCI 33 MHz

PCI Slot 1

32.768KHz

Super I/O

SIO 33 MHz

SATA 100 MHz Diff Pair

PCI Express 100 Mhz Diff Pair

LAN1

PCI Express 100 Mhz Diff Pair

Marvell 88SE6320

XDP 100MHz Diff Pair

JMB363

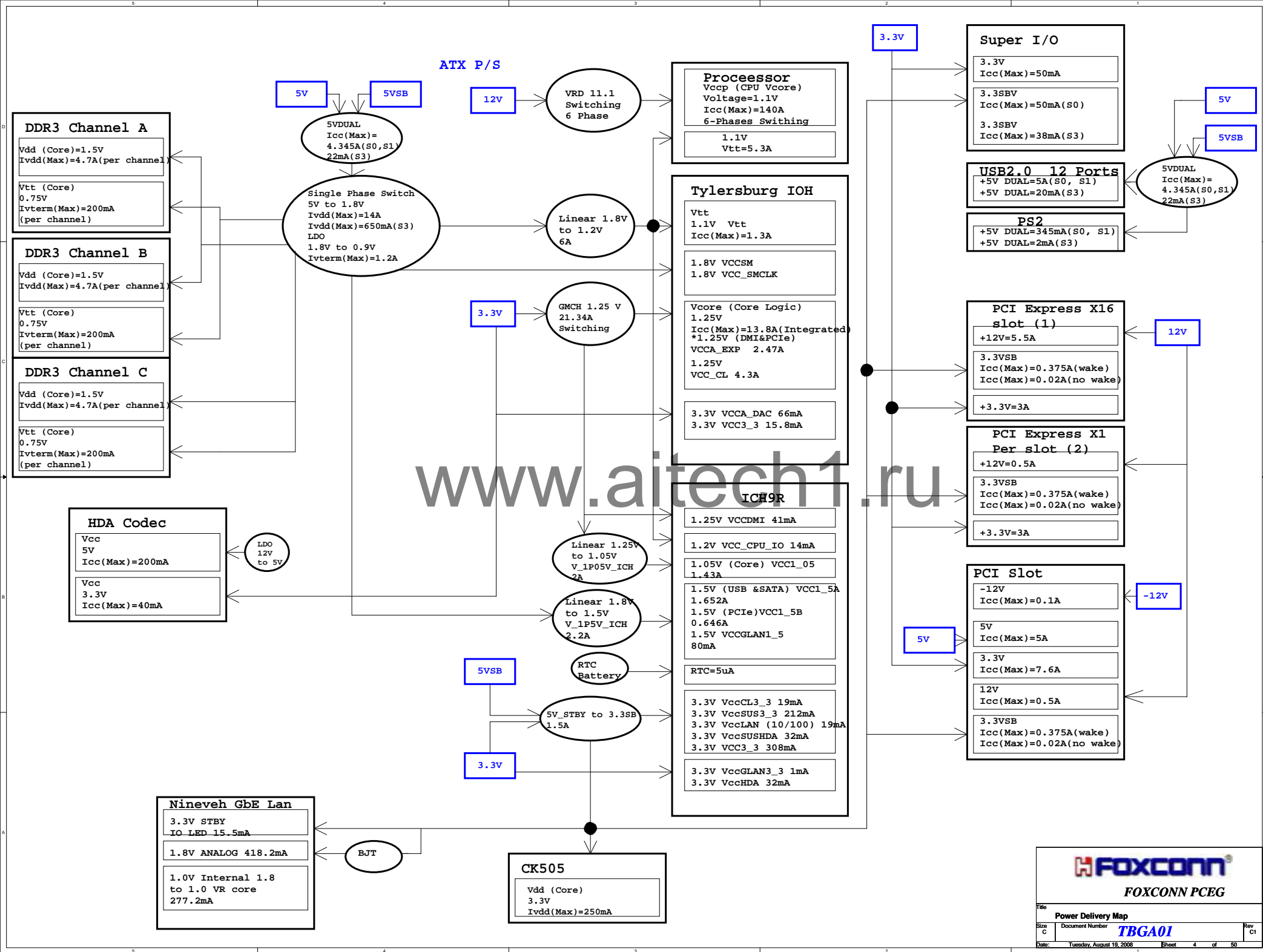
CK-505

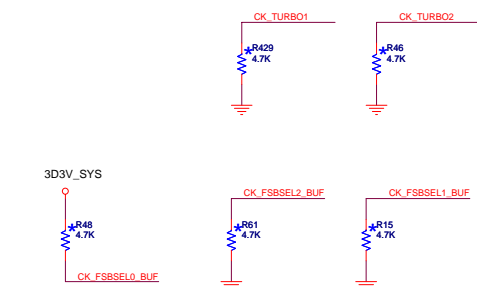
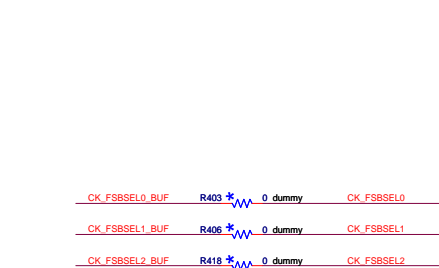
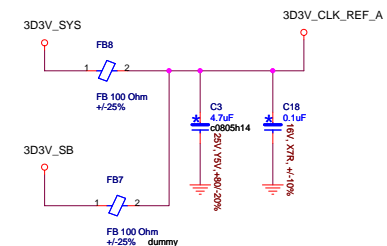
www.aitech1.ru

FOXCONN

FOXCONN PCEG

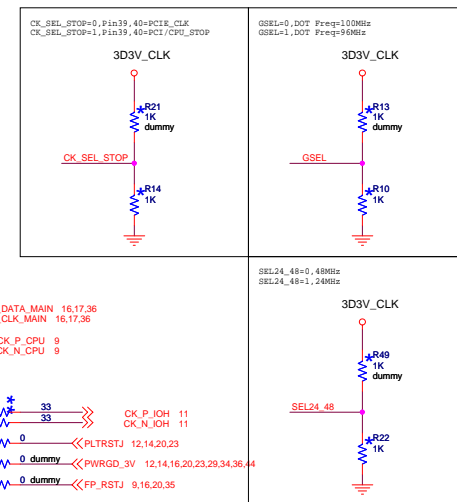
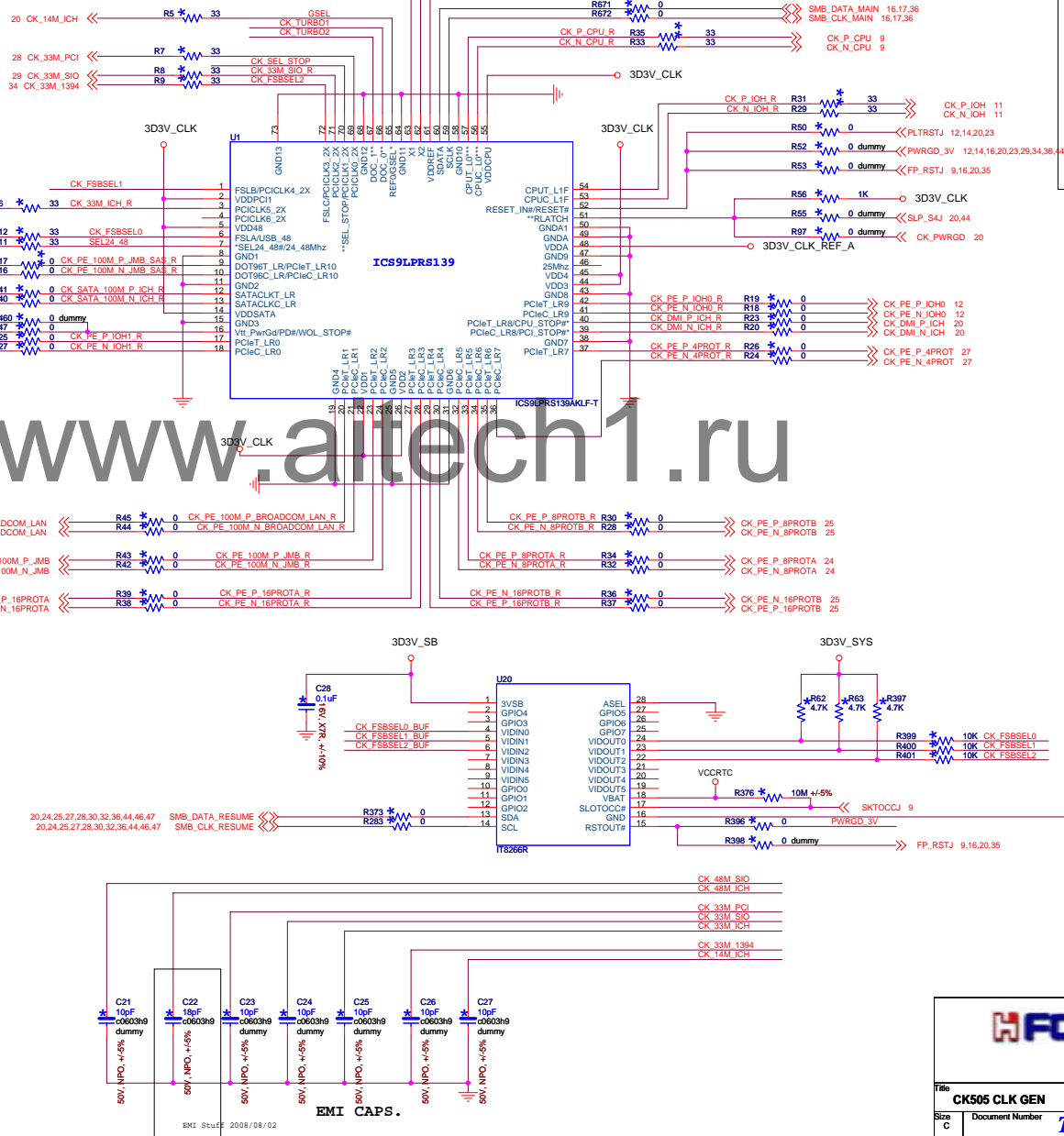
Clock Distribution			
File	Document Number	Rev	C1
Size	TBGA01		
C			
Date:	Tuesday, August 18, 2008	Sheet	3 of 50





BSEL TABLE

FS_C	FS_B	FS_A	FSB Frequency
0	0	1	133MHz(533)
0	1	0	200MHz(800)
0	0	0	266MHz(1066)
1	0	0	333MHz(1333)
1	1	0	400MHz(1600)



17 M_DATA_A[63..0]

M_DATA_A63	W4	DDR0_DQS_P0	T43	M_DQS_A0
M_DATA_A62	W4	DDR0_DQS_N0	U43	M_DQS_A1
M_DATA_A61	U3	DDR0_DQS_P1	L41	M_DQS_A2
M_DATA_A60	U1	DDR0_DQS_N1	M41	M_DQS_A3
M_DATA_A59	Y3	DDR0_DQS_P2	F41	M_DQS_A4
M_DATA_A58	Y2	DDR0_DQS_N2	G41	M_DQS_A5
M_DATA_A57	V1	DDR0_DQS_P3	B39	M_DQS_A6
M_DATA_A56	U4	DDR0_DQS_N3	D40	M_DQS_A7
M_DATA_A55	T3	DDR0_DQS_P4	E4	M_DQS_A8
M_DATA_A54	R4	DDR0_DQS_N4	F4	M_DQS_A9
M_DATA_A53	N3	DDR0_DQS_P5	K2	M_DQS_A10
M_DATA_A52	M3	DDR0_DQS_N5	R2	M_DQS_A11
M_DATA_A51	T2	DDR0_DQS_P6	R3	M_DQS_A12
M_DATA_A50	T1	DDR0_DQS_N6	U2	M_DQS_A13
M_DATA_A49	N2	DDR0_DQS_P7	D31	M_DQS_A14
M_DATA_A48	N1	DDR0_DQS_N7	D35	M_DQS_A15
M_DATA_A47	L2	DDR0_DQS_P8	V43	M_DQS_A16
M_DATA_A46	L3	DDR0_DQS_N8	V42	M_DQS_A17
M_DATA_A45	H3	DDR0_DQS_P9	M43	M_DQS_A18
M_DATA_A44	Q1	DDR0_DQS_N9	H42	M_DQS_A19
M_DATA_A43	M1	DDR0_DQS_P10	G43	M_DQS_A20
M_DATA_A42	L1	DDR0_DQS_N10	D39	M_DQS_A21
M_DATA_A41	H1	DDR0_DQS_P11	D5	M_DQS_A22
M_DATA_A40	H2	DDR0_DQS_N11	D4	M_DQS_A23
M_DATA_A39	G2	DDR0_DQS_P12	J2	M_DQS_A24
M_DATA_A38	F3	DDR0_DQS_N12	J1	M_DQS_A25
M_DATA_A37	C6	DDR0_DQS_P13	P2	M_DQS_A26
M_DATA_A36	R6	DDR0_DQS_N13	P1	M_DQS_A27
M_DATA_A35	Q3	DDR0_DQS_P14	V2	M_DQS_A28
M_DATA_A34	C4	DDR0_DQS_N14	V3	M_DQS_A29
M_DATA_A33	Q4	DDR0_DQS_P15	B36	M_DQS_A30
M_DATA_A32	R5	DDR0_DQS_N15	B35	M_DQS_A31
M_DATA_A31	B38	DDR0_DQS_P16		M_DQS_A32
M_DATA_A30	C38	DDR0_DQS_N16		M_DQS_A33
M_DATA_A29	D42	DDR0_DQS_P17		M_DQS_A34
M_DATA_A28	D37	DDR0_DQS_N17		M_DQS_A35
M_DATA_A27	D37	DDR0_DQS_P18		M_DQS_A36
M_DATA_A26	A38	DDR0_DQS_N18		M_DQS_A37
M_DATA_A25	C41	DDR0_DQS_P19		M_DQS_A38
M_DATA_A24	D40	DDR0_DQS_N19		M_DQS_A39
M_DATA_A23	F42	DDR0_DQS_P20		M_DQS_A40
M_DATA_A22	F43	DDR0_DQS_N20		M_DQS_A41
M_DATA_A21	K41	DDR0_DQS_P21		M_DQS_A42
M_DATA_A20	J42	DDR0_DQS_N21		M_DQS_A43
M_DATA_A19	H43	DDR0_DQS_P22		M_DQS_A44
M_DATA_A18	H41	DDR0_DQS_N22		M_DQS_A45
M_DATA_A17	L42	DDR0_DQS_P23		M_DQS_A46
M_DATA_A16	L42	DDR0_DQS_N23		M_DQS_A47
M_DATA_A15	L43	DDR0_DQS_P24		M_DQS_A48
M_DATA_A14	F41	DDR0_DQS_N24		M_DQS_A49
M_DATA_A13	F42	DDR0_DQS_P25		M_DQS_A50
M_DATA_A12	K43	DDR0_DQS_N25		M_DQS_A51
M_DATA_A11	K42	DDR0_DQS_P26		M_DQS_A52
M_DATA_A10	K43	DDR0_DQS_N26		M_DQS_A53
M_DATA_A9	N43	DDR0_DQS_P27		M_DQS_A54
M_DATA_A8	N41	DDR0_DQS_N27		M_DQS_A55
M_DATA_A7	T42	DDR0_DQS_P28		M_DQS_A56
M_DATA_A6	U41	DDR0_DQS_N28		M_DQS_A57
M_DATA_A5	W42	DDR0_DQS_P29		M_DQS_A58
M_DATA_A4	W40	DDR0_DQS_N29		M_DQS_A59
M_DATA_A3	R42	DDR0_DQS_P30		M_DQS_A60
M_DATA_A2	R43	DDR0_DQS_N30		M_DQS_A61
M_DATA_A1	V41	DDR0_DQS_P31		M_DQS_A62
M_DATA_A0	W41	DDR0_DQS_N31		M_DQS_A63

C34 DDR0_ECC_7
B34 DDR0_ECC_6
A37 DDR0_ECC_5
C37 DDR0_ECC_4
C33 DDR0_ECC_3
F36 DDR0_ECC_2
A36 DDR0_ECC_1
C36 DDR0_ECC_0

B_F_0_7_CPU

1 OF 12

>>>M_DQS_A[7..0] 17

18 M_DATA_B[63..0]

M_DATA_B63	W9	DDR1_DQ_63	Y38	M_DQS_B0
M_DATA_B62	A47	DDR1_DQ_62	Y37	M_DQS_B1
M_DATA_B61	W5	DDR1_DQ_61	R38	M_DQS_B2
M_DATA_B60	W9	DDR1_DQ_60	R37	M_DQS_B3
M_DATA_B59	W10	DDR1_DQ_59	L36	M_DQS_B4
M_DATA_B58	Y10	DDR1_DQ_58	L35	M_DQS_B5
M_DATA_B57	W7	DDR1_DQ_57	L34	M_DQS_B6
M_DATA_B56	W6	DDR1_DQ_56	L33	M_DQS_B7
M_DATA_B55	R7	DDR1_DQ_55	L32	M_DQS_B8
M_DATA_B54	R8	DDR1_DQ_54	L31	M_DQS_B9
M_DATA_B53	M6	DDR1_DQ_53	L30	M_DQS_B10
M_DATA_B52	J4	DDR1_DQ_52	L29	M_DQS_B11
M_DATA_B51	T5	DDR1_DQ_51	L28	M_DQS_B12
M_DATA_B50	R5	DDR1_DQ_50	L27	M_DQS_B13
M_DATA_B49	K5	DDR1_DQ_49	L26	M_DQS_B14
M_DATA_B48	K6	DDR1_DQ_48	L25	M_DQS_B15
M_DATA_B47	K4	DDR1_DQ_47	L24	M_DQS_B16
M_DATA_B46	G5	DDR1_DQ_46	L23	M_DQS_B17
M_DATA_B45	G9	DDR1_DQ_45	L22	M_DQS_B18
M_DATA_B44	H9	DDR1_DQ_44	L21	M_DQS_B19
M_DATA_B43	H4	DDR1_DQ_43	L20	M_DQS_B20
M_DATA_B42	G6	DDR1_DQ_42	L19	M_DQS_B21
M_DATA_B41	G6	DDR1_DQ_41	L18	M_DQS_B22
M_DATA_B40	H8	DDR1_DQ_40	L17	M_DQS_B23
M_DATA_B39	F6	DDR1_DQ_39	L16	M_DQS_B24
M_DATA_B38	D6	DDR1_DQ_38	L15	M_DQS_B25
M_DATA_B37	G8	DDR1_DQ_37	L14	M_DQS_B26
M_DATA_B36	F10	DDR1_DQ_36	L13	M_DQS_B27
M_DATA_B35	F5	DDR1_DQ_35	L12	M_DQS_B28
M_DATA_B34	E5	DDR1_DQ_34	L11	M_DQS_B29
M_DATA_B33	E8	DDR1_DQ_33	L10	M_DQS_B30
M_DATA_B32	E9	DDR1_DQ_32	L9	M_DQS_B31
M_DATA_B31	K30	DDR1_DQ_31	L8	M_DQS_B32
M_DATA_B30	L32	DDR1_DQ_30	L7	M_DQS_B33
M_DATA_B29	H34	DDR1_DQ_29	L6	M_DQS_B34
M_DATA_B28	J34	DDR1_DQ_28	L5	M_DQS_B35
M_DATA_B27	J32	DDR1_DQ_27	L4	M_DQS_B36
M_DATA_B26	K32	DDR1_DQ_26	L3	M_DQS_B37
M_DATA_B25	L33	DDR1_DQ_25	L2	M_DQS_B38
M_DATA_B24	H33	DDR1_DQ_24	L1	M_DQS_B39
M_DATA_B23	H36	DDR1_DQ_23	L0	M_DQS_B40
M_DATA_B22	J36	DDR1_DQ_22		M_DQS_B41
M_DATA_B21	M36	DDR1_DQ_21		M_DQS_B42
M_DATA_B20	N34	DDR1_DQ_20		M_DQS_B43
M_DATA_B19	J35	DDR1_DQ_19		M_DQS_B44
M_DATA_B18	K35	DDR1_DQ_18		M_DQS_B45
M_DATA_B17	M34	DDR1_DQ_17		M_DQS_B46
M_DATA_B16	N38	DDR1_DQ_16		M_DQS_B47
M_DATA_B15	N38	DDR1_DQ_15		M_DQS_B48
M_DATA_B14	N37	DDR1_DQ_14		M_DQS_B49
M_DATA_B13	R36	DDR1_DQ_13		M_DQS_B50
M_DATA_B12	R34	DDR1_DQ_12		M_DQS_B51
M_DATA_B11	N39	DDR1_DQ_11		M_DQS_B52
M_DATA_B10	P39	DDR1_DQ_10		M_DQS_B53
M_DATA_B9	P36	DDR1_DQ_9		M_DQS_B54
M_DATA_B8	P34	DDR1_DQ_8		M_DQS_B55
M_DATA_B7	Y39	DDR1_DQ_7		M_DQS_B56
M_DATA_B6	Y40	DDR1_DQ_6		M_DQS_B57
M_DATA_B5	AB36	DDR1_DQ_5		M_DQS_B58
M_DATA_B4	AA35	DDR1_DQ_4		M_DQS_B59
M_DATA_B3	Y34	DDR1_DQ_3		M_DQS_B60
M_DATA_B2	Y45	DDR1_DQ_2		M_DQS_B61
M_DATA_B1	AA36	DDR1_DQ_1		M_DQS_B62
M_DATA_B0	AA37	DDR1_DQ_0		M_DQS_B63

G35 DDR1_ECC_7
E34 DDR1_ECC_6
F37 DDR1_ECC_5
E37 DDR1_ECC_4
G36 DDR1_ECC_3
F33 DDR1_ECC_2
D36 DDR1_ECC_1
D36 DDR1_ECC_0

B_F_0_7_CPU

2 OF 12

>>>M_DQS_B[7..0] 18

AM6	RSVD_1	RSVD_45	AC8
AM5	RSVD_2	RSVD_46	AD8
AM4	RSVD_3	RSVD_47	AD5
AM7	RSVD_4	RSVD_48	AD5
AM6	RSVD_5	RSVD_49	AD6
AM5	RSVD_6	RSVD_50	AD7
AM4	RSVD_7	RSVD_51	AD6
AM3	RSVD_8	RSVD_52	AC6
AP3	RSVD_9	RSVD_53	AC4
AP4	RSVD_10	RSVD_54	AE3
AM2	RSVD_11	RSVD_55	AE3
AM1	RSVD_12	RSVD_56	AE4
AN1	RSVD_13	RSVD_57	AE3
AM3	RSVD_14	RSVD_58	AE3
AP2	RSVD_15	RSVD_59	AD2
AN2	RSVD_16	RSVD_60	AE1
AR4	RSVD_17	RSVD_61	AD1
AR5	RSVD_18	RSVD_62	AD1
AT1	RSVD_19	RSVD_63	AE2
AR1	RSVD_20	RSVD_64	AE3
AT2	RSVD_21	RSVD_65	AH2
AT3	RSVD_22	RSVD_66	AH3
AU4	RSVD_23	RSVD_67	AH3
AU3	RSVD_24	RSVD_68	AH4
AW4	RSVD_25	RSVD_69	AK1
AU5	RSVD_26	RSVD_70	AJ1
AU7	RSVD_27	RSVD_71	AJ3
AU6	RSVD_28	RSVD_72	AJ2
AY6	RSVD_29	RSVD_73	AG7
AYS	RSVD_30	RSVD_74	AG6
BA7	RSVD_31	RSVD_75	AH4
BA6	RSVD_32	RSVD_76	AK4
AV5	RSVD_33	RSVD_77	AK6
AV6	RSVD_34	RSVD_78	AK6
AV8	RSVD_35	RSVD_79	AH6
BA8	RSVD_36	RSVD_80	AJ6
AV7	RSVD_37	RSVD_81	AJ8
AU8	RSVD_38	RSVD_82	AJ7
AV8	RSVD_39	RSVD_83	AH8
AV8	RSVD_40	RSVD_84	AH8
AT6	RSVD_41		
AR6	RSVD_42		
AE6	RSVD_43		
AE6	RSVD_44		

B_F_0_7_CPU

8 OF 12

11	CSIO_DRXP19	AP38	CSIO_DRX_DP19
11	CSIO_DRXN19	AR38	CSIO_DRX_DP19
11	CSIO_DRXP18	AN39	CSIO_DRX_DP18
11	CSIO_DRXN18	AR39	CSIO_DRX_DP18
11	CSIO_DRXP17	AP41	CSIO_DRX_DP17
11	CSIO_DRXN17	AR41	CSIO_DRX_DP17
11	CSIO_DRXP16	AM42	CSIO_DRX_DP16
11	CSIO_DRXN16	AR42	CSIO_DRX_DP16
11	CSIO_DRXP15	AN40	CSIO_DRX_DP15
11	CSIO_DRXN15	AR40	CSIO_DRX_DP15
11	CSIO_DRXP14	AM43	CSIO_DRX_DP14
11	CSIO_DRXN14	AR43	CSIO_DRX_DP14
11	CSIO_DRXP13	AP42	CSIO_DRX_DP13
11	CSIO_DRXN13	AR42	CSIO_DRX_DP13
11	CSIO_DRXP12	AT40	CSIO_DRX_DP12
11	CSIO_DRXN12	AR40	CSIO_DRX_DP12
11	CSIO_DRXP11	AT43	CSIO_DRX_DP11
11	CSIO_DRXN11	AR43	CSIO_DRX_DP11
11	CSIO_DRXP10	AF2	CSIO_DRX_DP10
11	CSIO_DRXN10	AR42	CSIO_DRX_DP10
11	CSIO_DRXP9	AU40	CSIO_DRX_DP9
11	CSIO_DRXN9	AR40	CSIO_DRX_DP9
11	CSIO_DRXP8	AW40	CSIO_DRX_DP8
11	CSIO_DRXN8	AR40	CSIO_DRX_DP8
11	CSIO_DRXP7	AU40	CSIO_DRX_DP7
11	CSIO_DRXN7	AR40	CSIO_DRX_DP7
11	CSIO_DRXP6	AJ42	CSIO_DRX_DP6
11	CSIO_DRXN6	BA38	CSIO_DRX_DP6
11	CSIO_DRXP5	AV38	CSIO_DRX_DP5
11	CSIO_DRXN5	BA36	CSIO_DRX_DP5
11	CSIO_DRXP4	AW38	CSIO_DRX_DP4
11	CSIO_DRXN4	BA36	CSIO_DRX_DP4
11	CSIO_DRXP3	BA37	CSIO_DRX_DP3
11	CSIO_DRXN3	AW36	CSIO_DRX_DP3
11	CSIO_DRXP2	AV36	CSIO_DRX_DP2
11	CSIO_DRXN2	AW37	CSIO_DRX_DP2
11	CSIO_DRXP1	AU38	CSIO_DRX_DP1
11	CSIO_DRXN1	AV38	CSIO_DRX_DP1
11	CSIO_DRXP0	AC38	CSIO_DRX_DP0
11	CSIO_DRXN0	AU37	CSIO_DRX_DP0

CSIO_CLK_RXP AR41
CSIO_CLK_RXN AR42
CSIO_CLK_TXP AG42
CSIO_CLK_TXN AF42

B_F_0_7_CPU

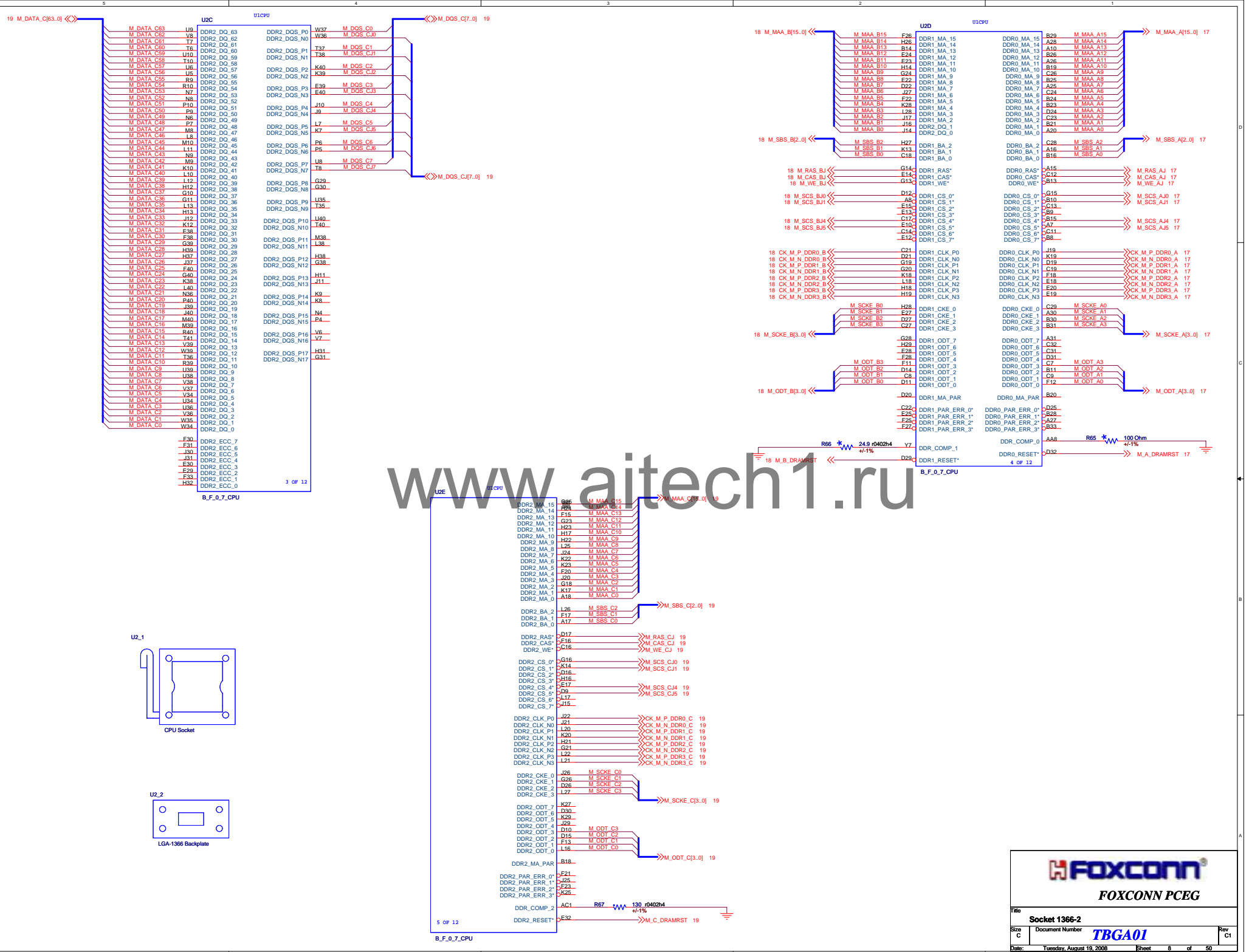
7 OF 12

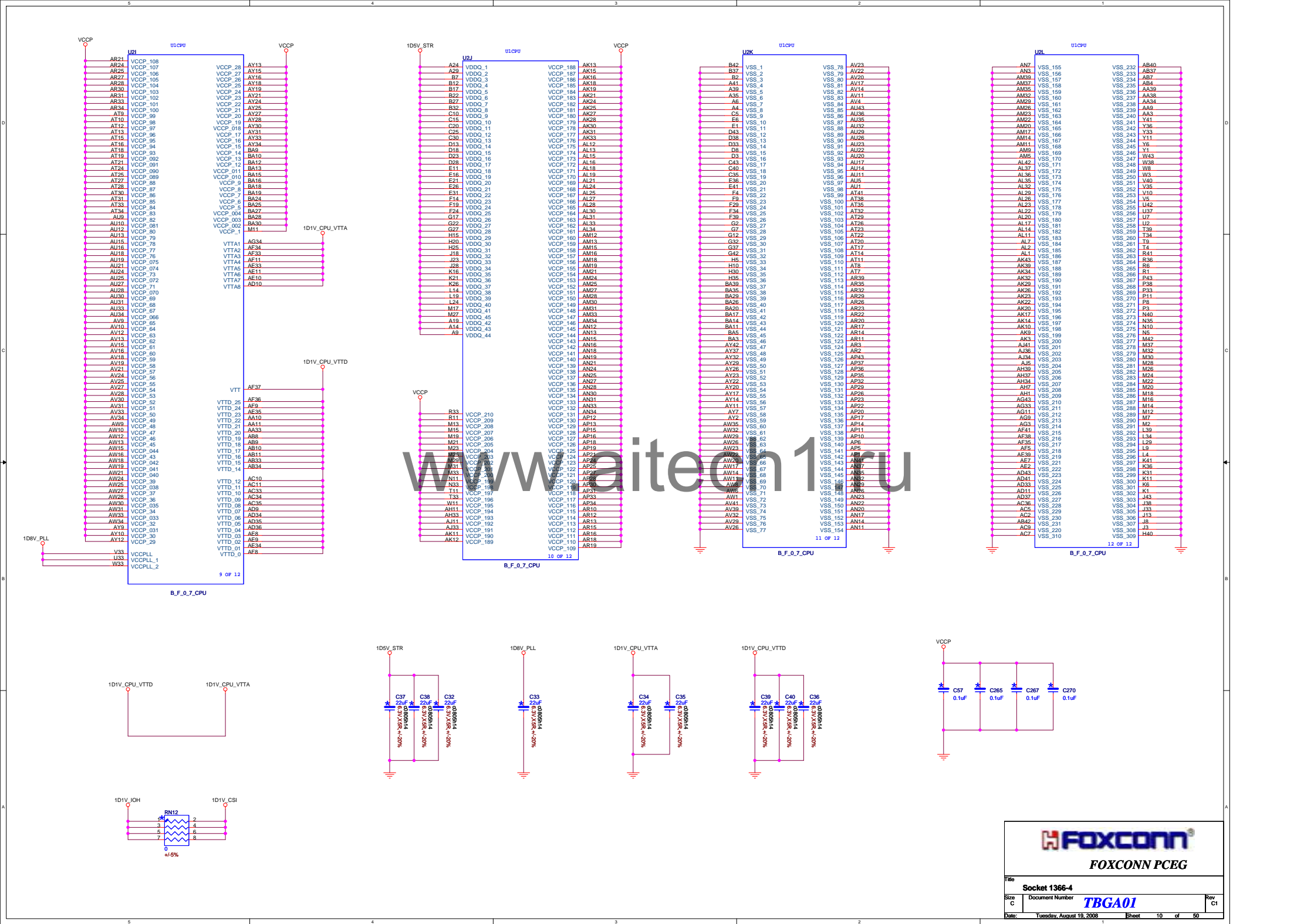
CSIO_CMP0

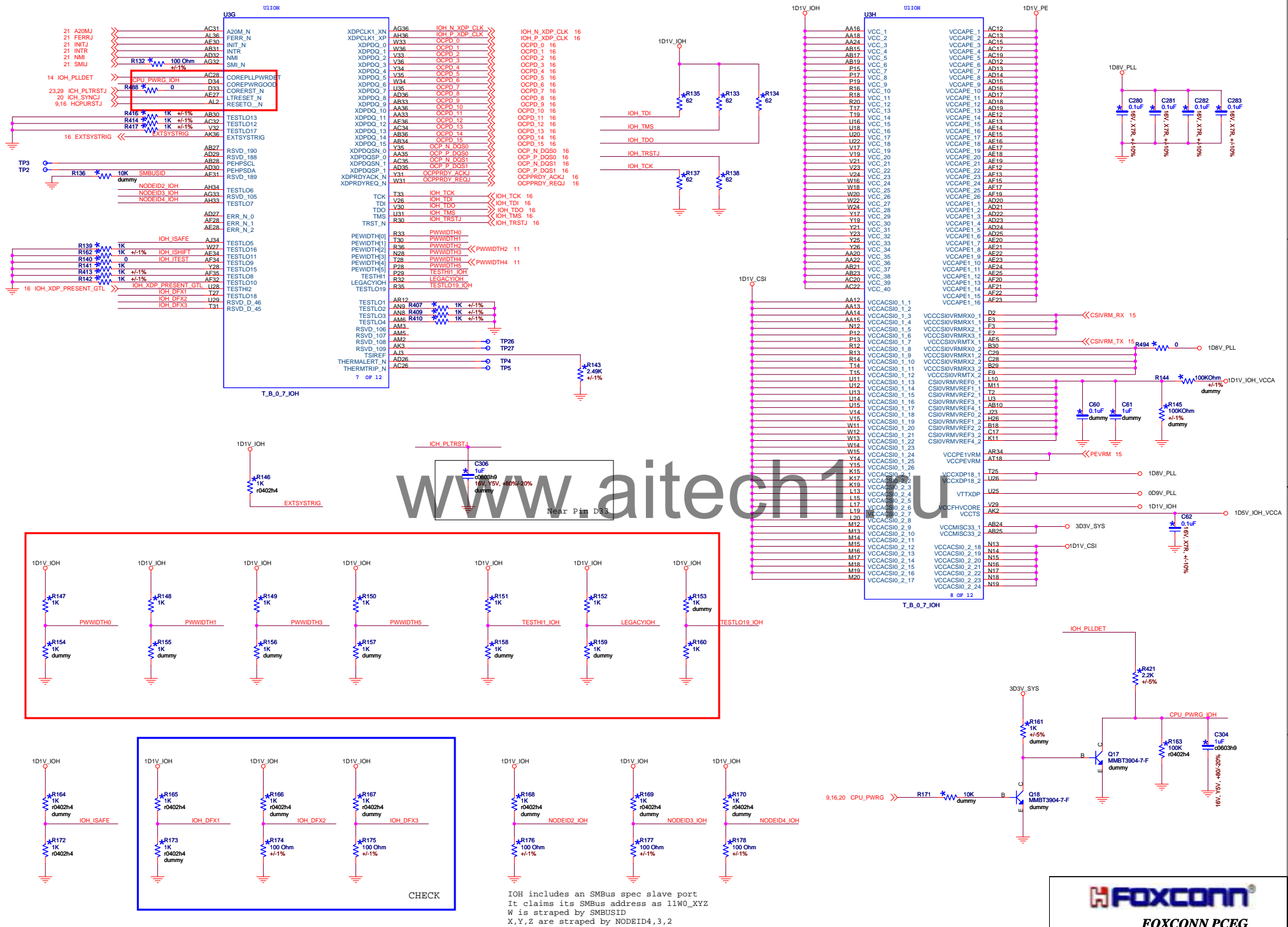
AL43
R64
+/-1%
r0603h6

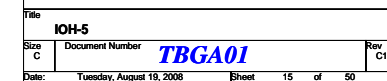
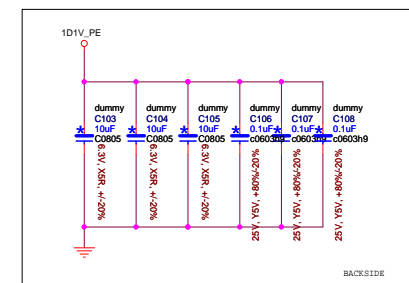
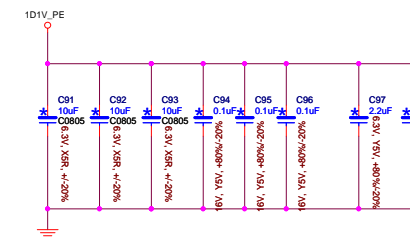
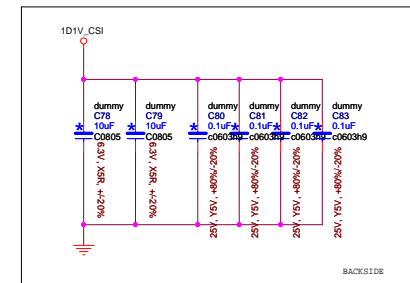
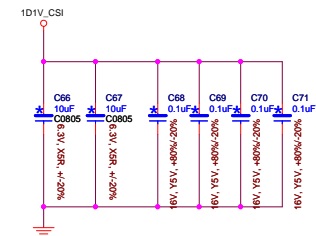
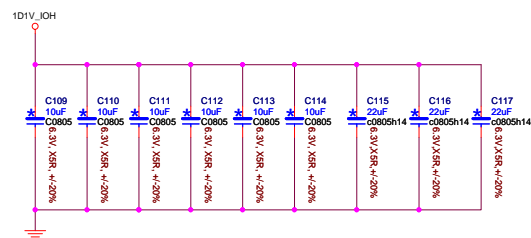
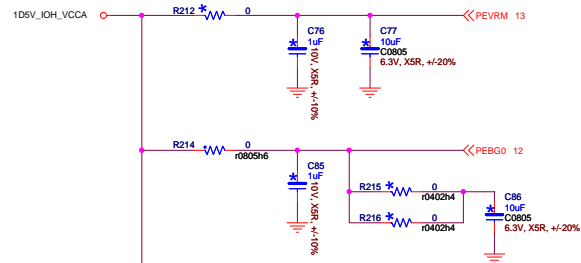
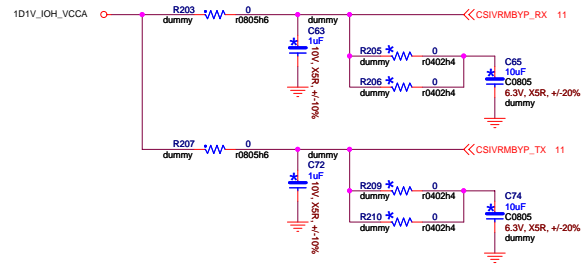
FOXCONN
FOXCONN PCEG

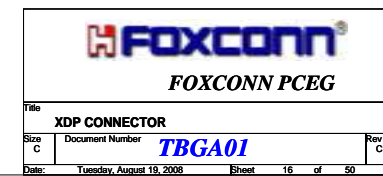
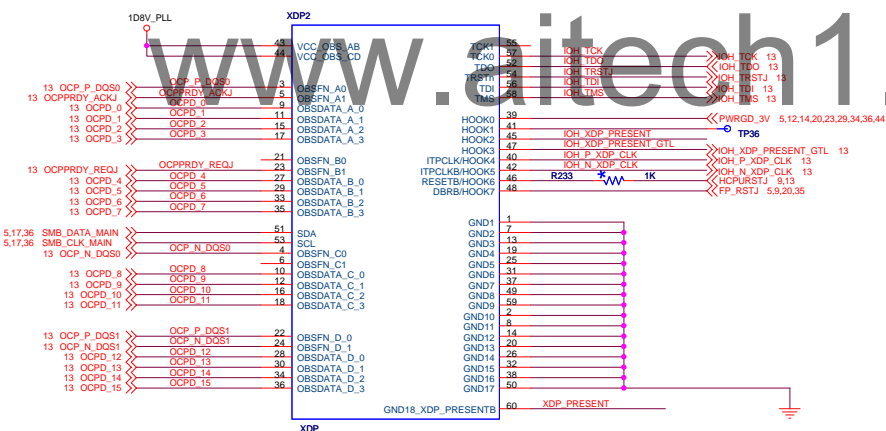
File	Socket 1366-1	Rev	C1
Size	Document Number	TBGA01	
Date	Tuesday, August 18, 2008	Sheet	7 of 50

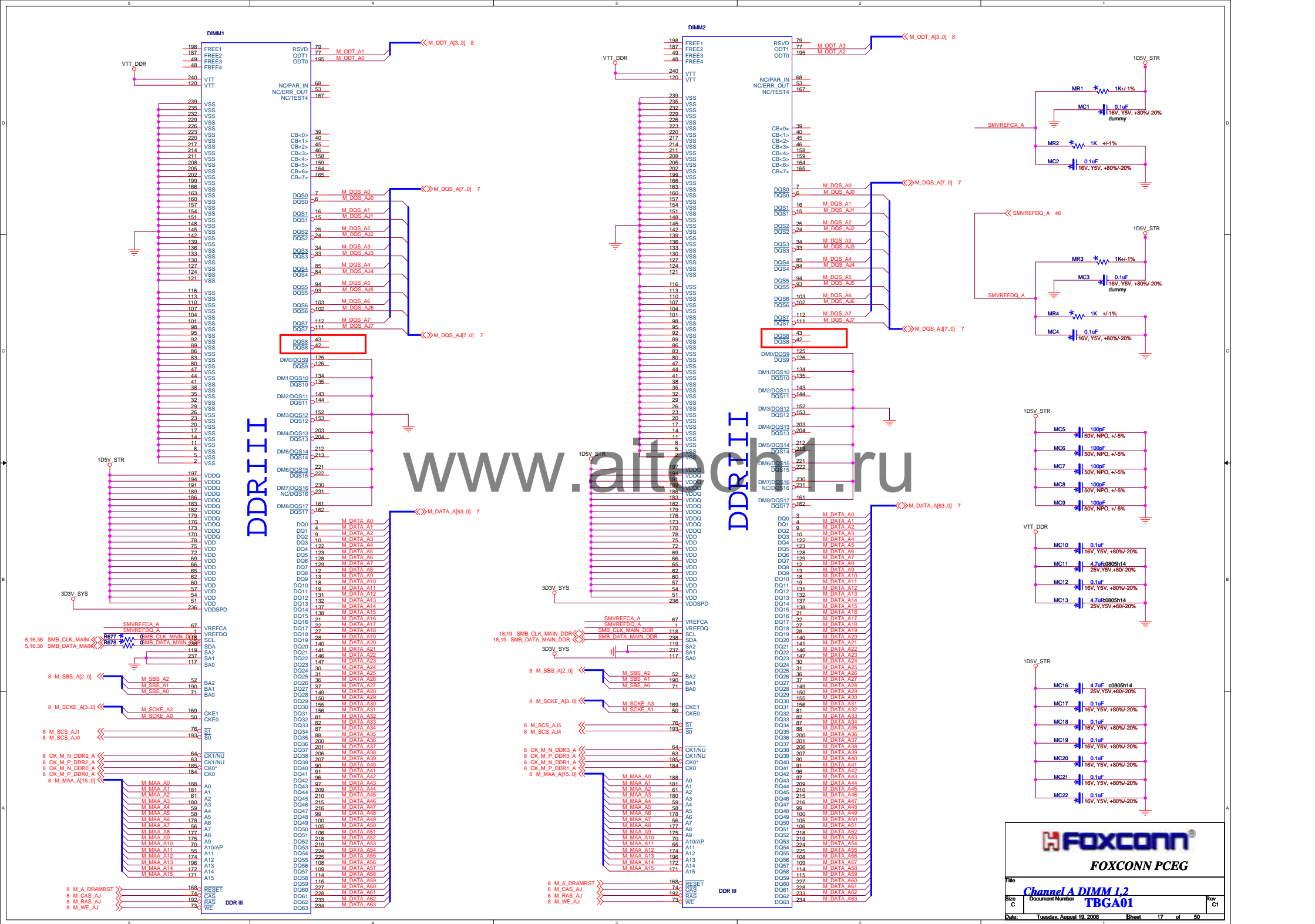


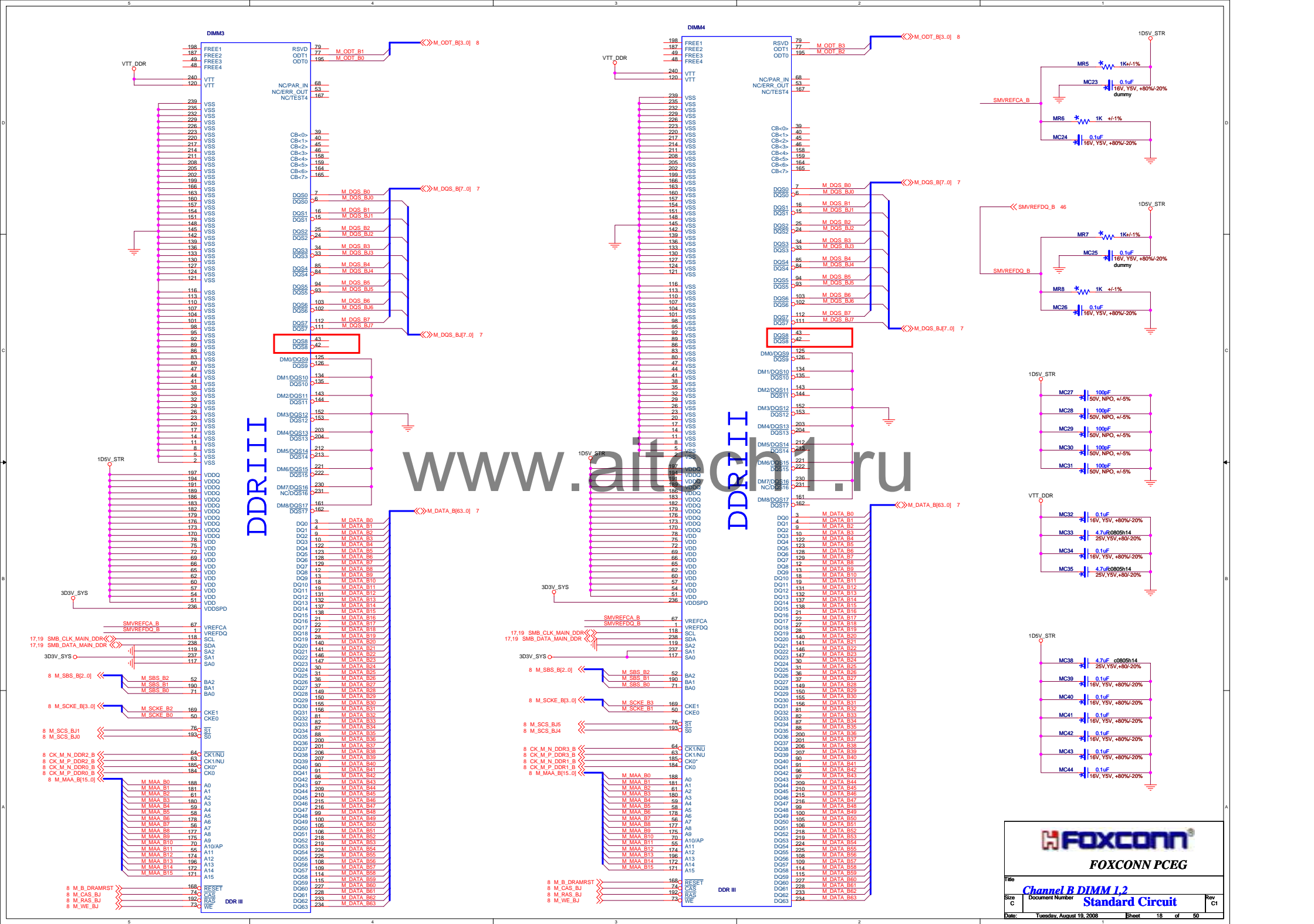


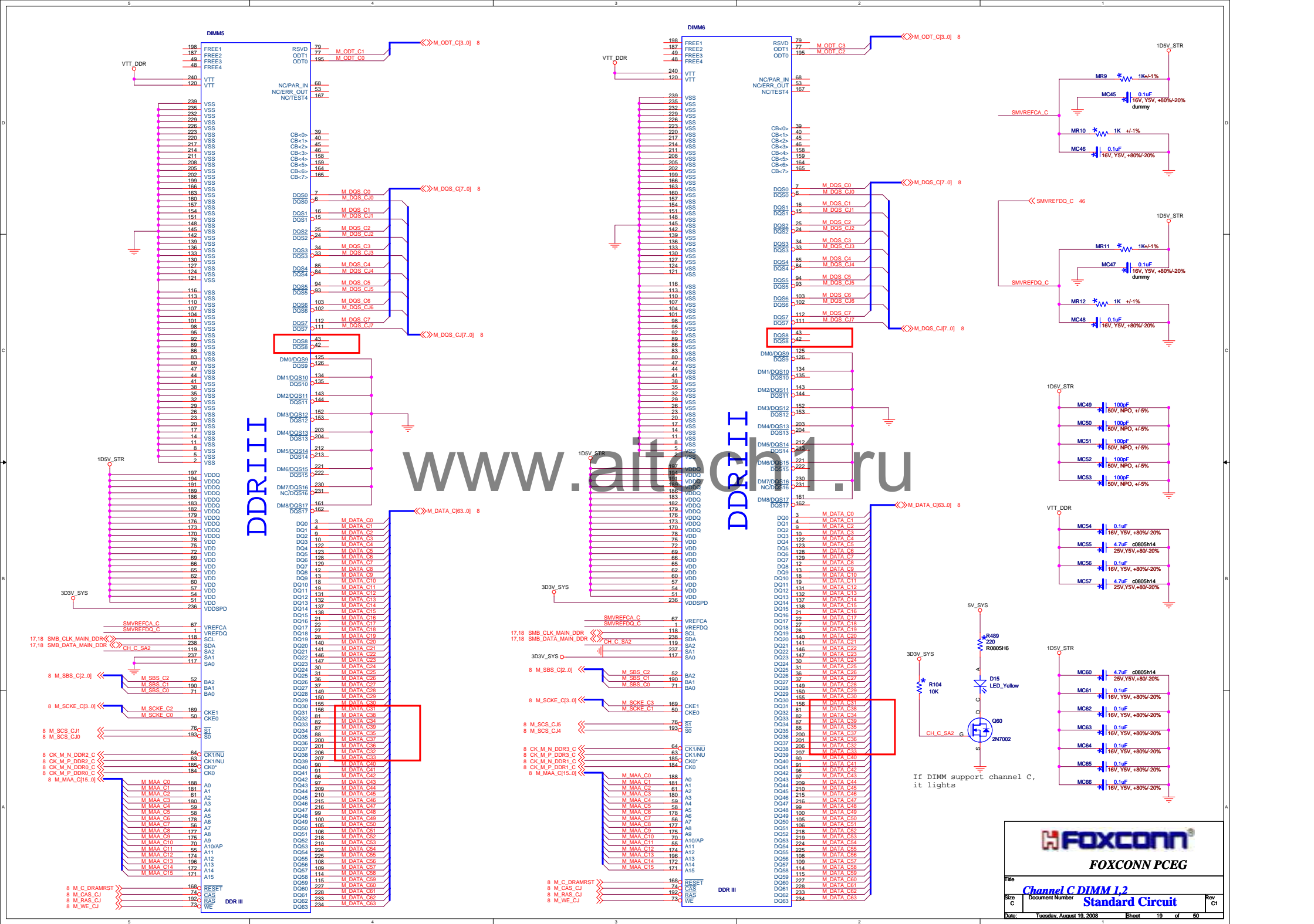


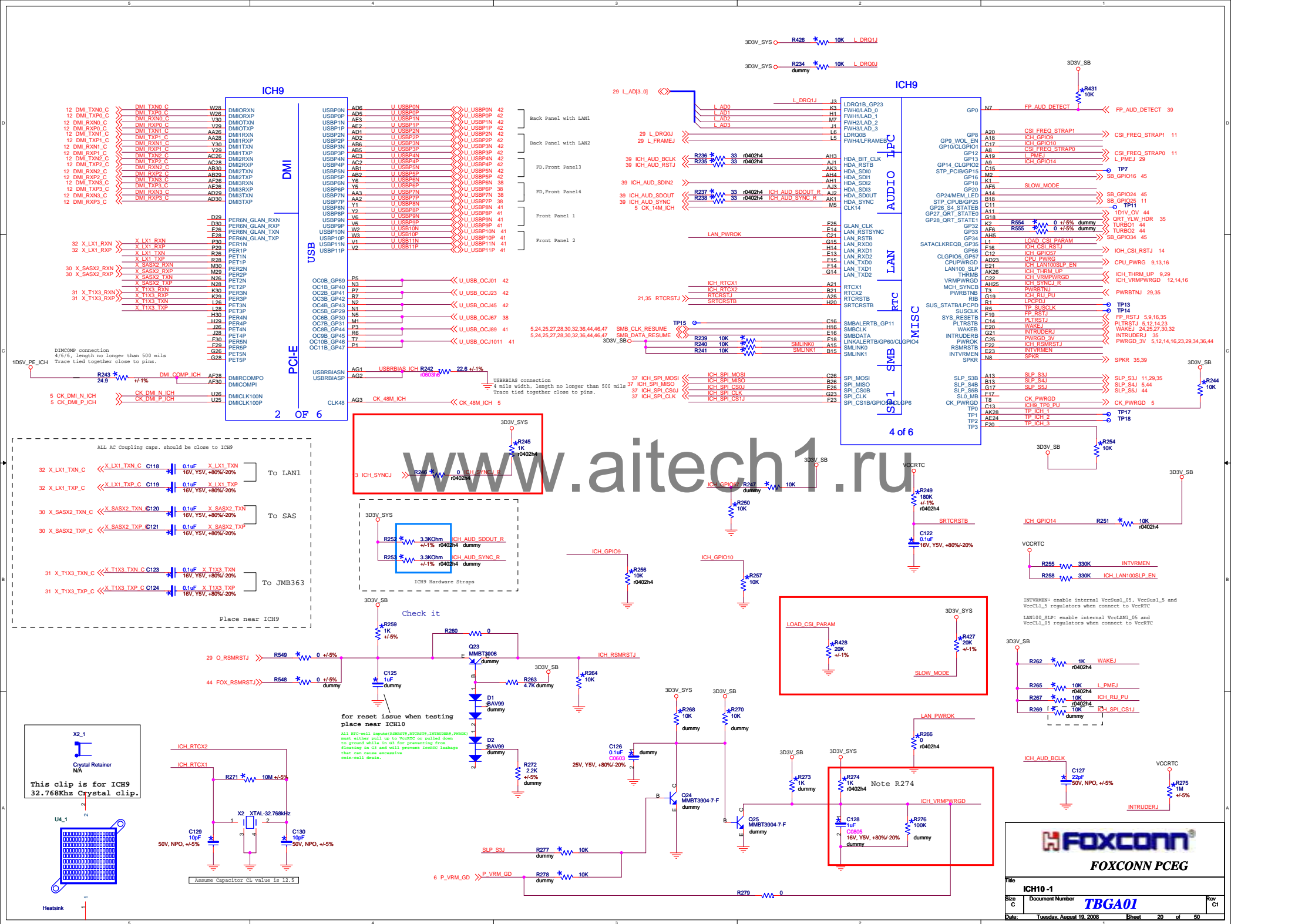


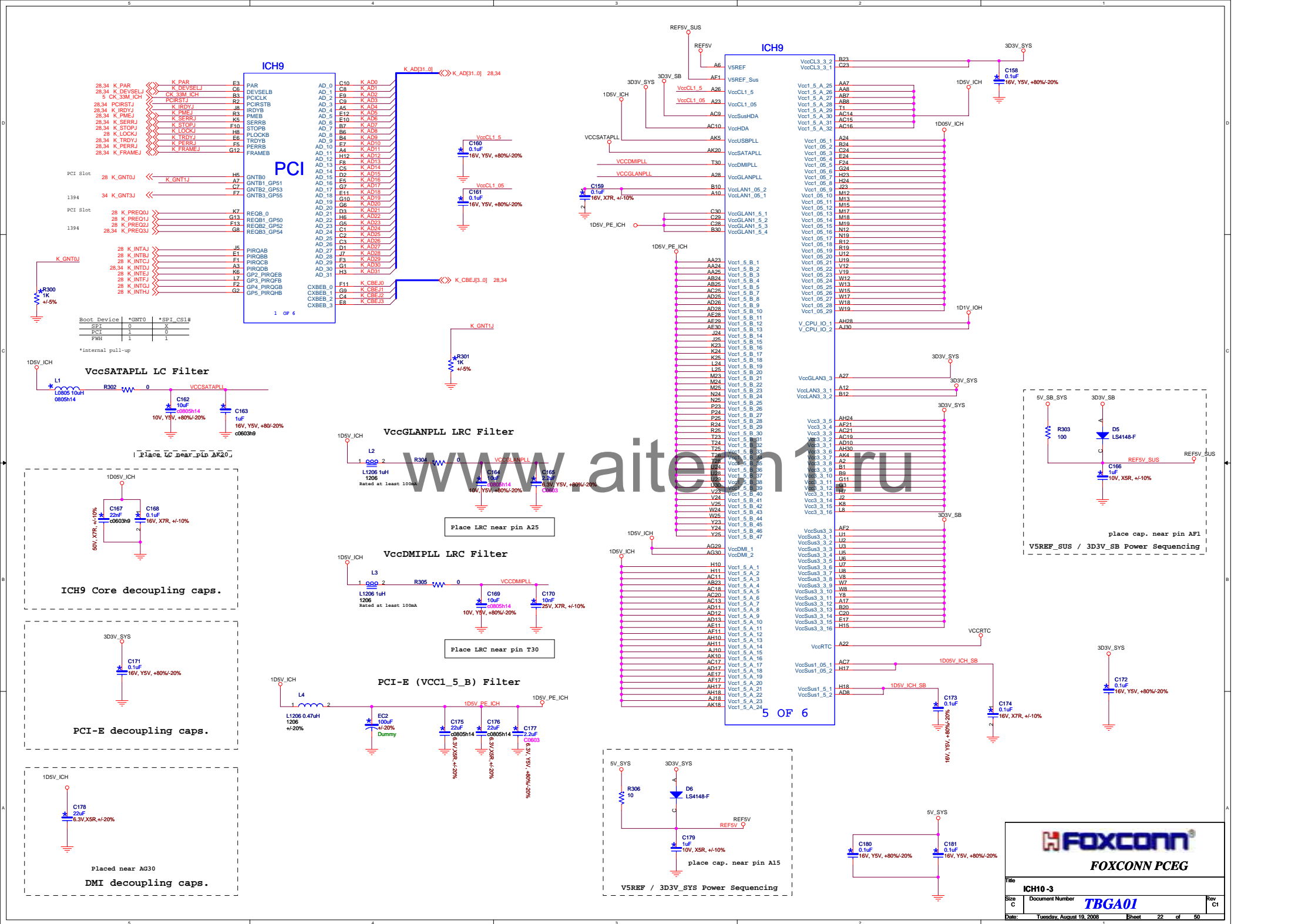






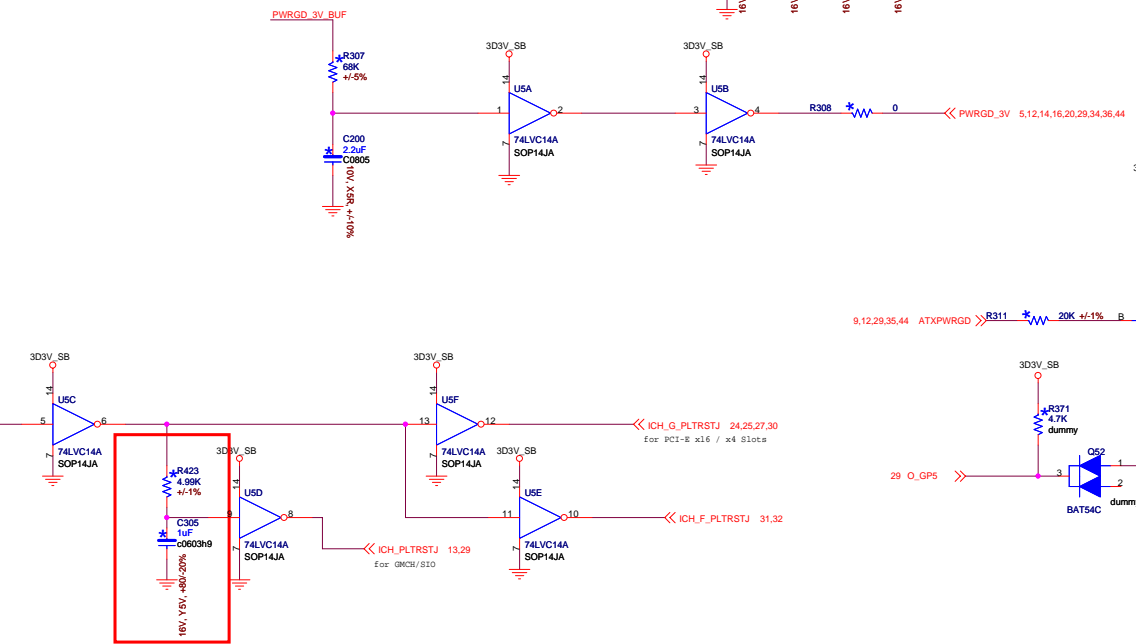
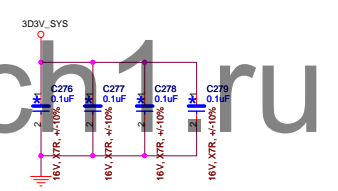
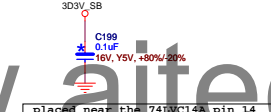
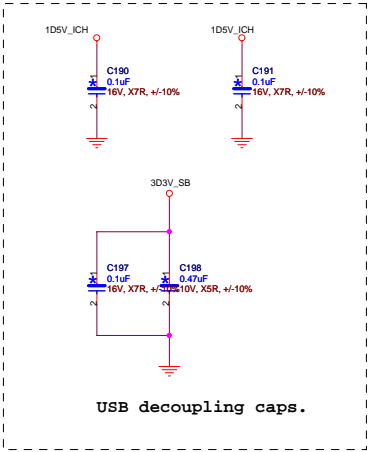
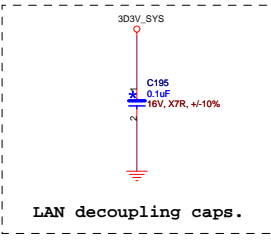
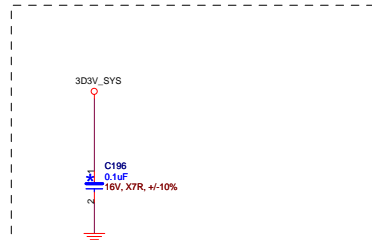
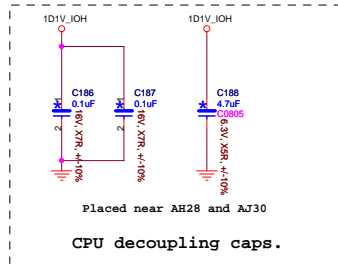
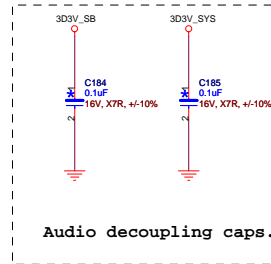
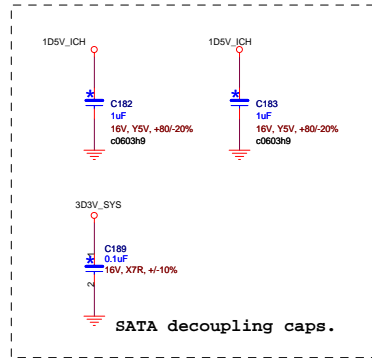






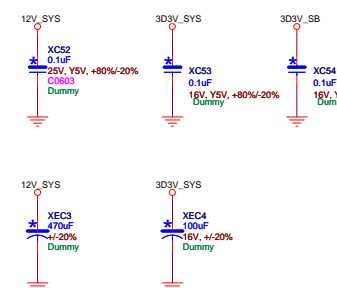
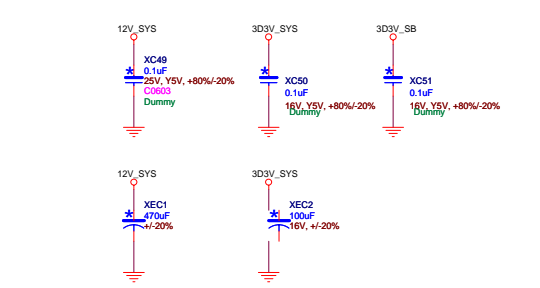
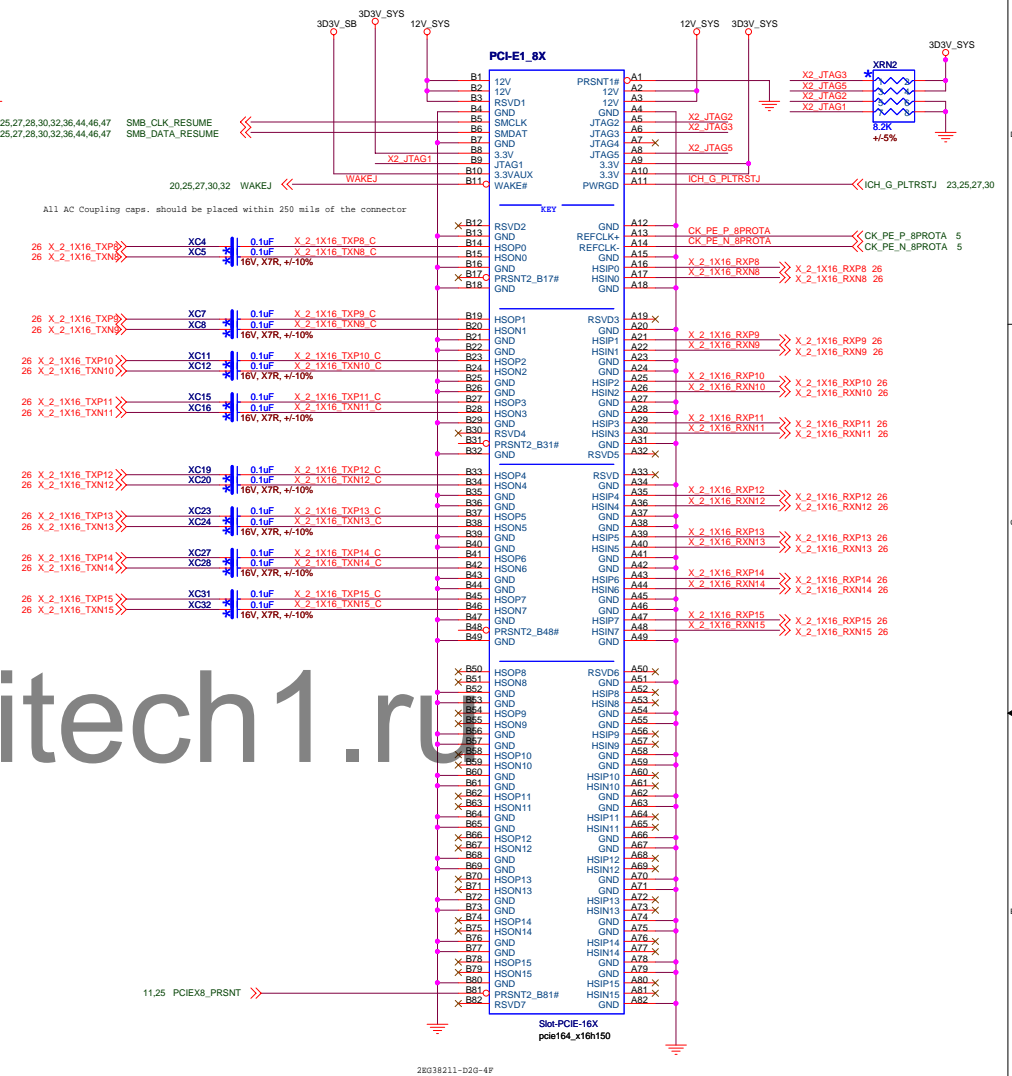
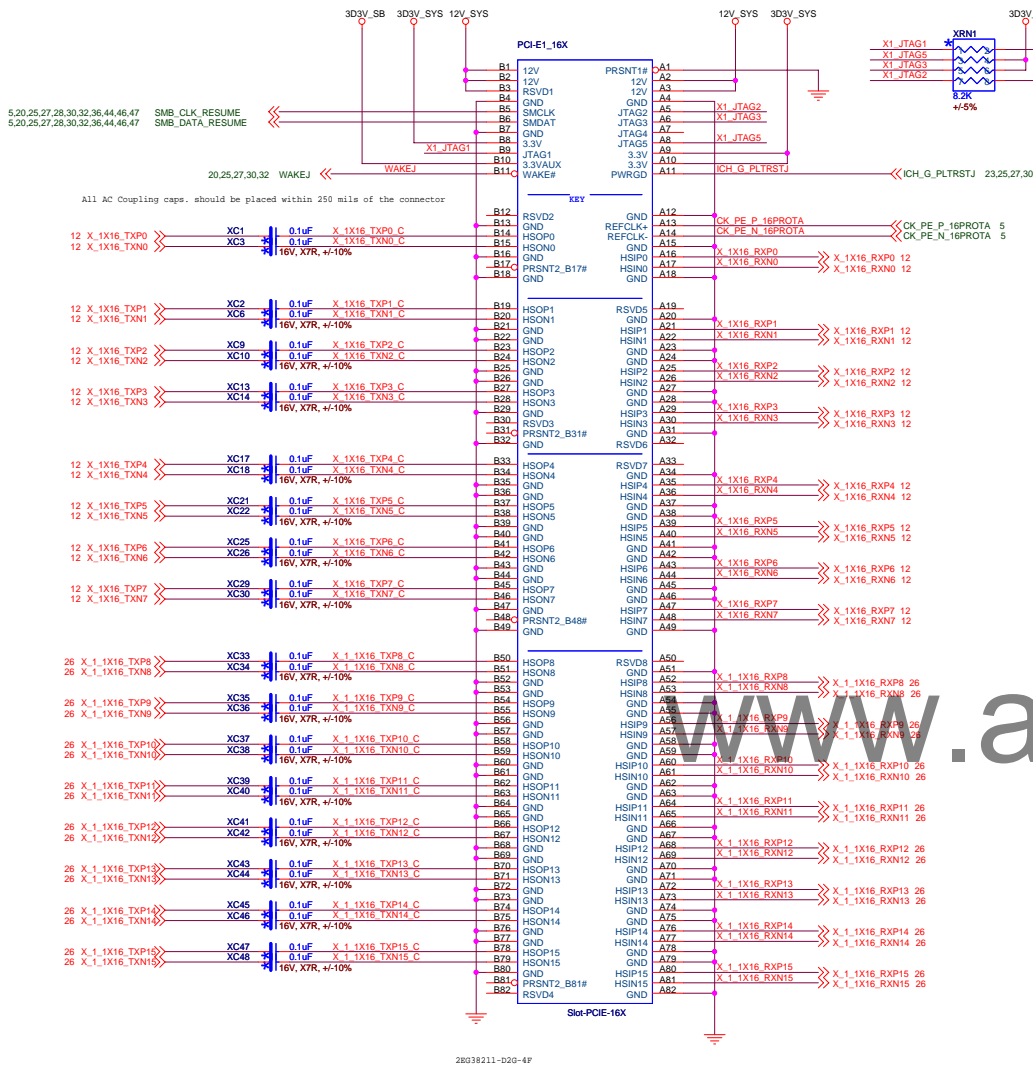
ICH9		
G30	VSS_100	H13
G29	VSS_101	H19
G25	VSS_102	H2
G16	VSS_103	H22
F9	VSS_104	H25
F28	VSS_105	H26
F21	VSS_106	H28
F12	VSS_107	H9
E30	VSS_108	H29
E20	VSS_109	H30
E22	VSS_110	J6
E18	VSS_111	K26
E15	VSS_112	L2
D28	VSS_113	L23
B8	VSS_114	L30
B5	VSS_115	M14
B22	VSS_116	M16
B19	VSS_117	M28
B14	VSS_118	M29
B11	VSS_119	M2
B8	VSS_120	M6
B2	VSS_121	M8
B19	VSS_122	M13
B14	VSS_123	M14
B11	VSS_124	M15
B8	VSS_125	M16
B2	VSS_126	M18
B19	VSS_127	M28
B14	VSS_128	N13
B11	VSS_129	N14
B8	VSS_130	N15
B2	VSS_131	N16
B19	VSS_132	N17
B14	VSS_133	N18
B11	VSS_134	N23
B8	VSS_135	N29
B2	VSS_136	N30
B19	VSS_137	P12
B14	VSS_138	P13
B11	VSS_139	P14
B8	VSS_140	P15
B2	VSS_141	P16
B19	VSS_142	P17
B14	VSS_143	P18
B11	VSS_144	P19
B8	VSS_145	P2
B2	VSS_146	P26
B19	VSS_147	P28
B14	VSS_148	P6
B11	VSS_149	R13
B8	VSS_150	R14
B2	VSS_151	R15
B19	VSS_152	R16
B14	VSS_153	R17
B11	VSS_154	R18
B8	VSS_155	R19
B2	VSS_156	R23
B19	VSS_157	R29
B14	VSS_158	R30
B11	VSS_159	R8
B8	VSS_160	T12
B2	VSS_161	T13
B19	VSS_162	T14
B14	VSS_163	T15
B11	VSS_164	T16
B8	VSS_165	T17
B2	VSS_166	T19
B19	VSS_167	T2
B14	VSS_168	T29
B11	VSS_169	T3
B8	VSS_170	T11
B2	VSS_171	T12
B19	VSS_172	T13
B14	VSS_173	T14
B11	VSS_174	T15
B8	VSS_175	T16
B2	VSS_176	T17
B19	VSS_177	T19
B14	VSS_178	T2
B11	VSS_179	T29
B8	VSS_180	T3
B2	VSS_181	T11
B19	VSS_182	T12
B14	VSS_183	T13
B11	VSS_184	T14
B8	VSS_185	T15
B2	VSS_186	T16
B19	VSS_187	T17
B14	VSS_188	T19
B11	VSS_189	T2
B8	VSS_190	T29
B2	VSS_191	T3
B19	VSS_192	T11
B14	VSS_193	T12
B11	VSS_194	T13
B8	VSS_195	T14
B2	VSS_196	T15
B19	VSS_197	T16
B14	VSS_198	T17
B11	VSS_199	T19
B8	VSS_200	T2
B2	VSS_201	T29
B19	VSS_202	T3
B14	VSS_203	T11
B11	VSS_204	T12
B8	VSS_205	T13
B2	VSS_206	T14
B19	VSS_207	T15
B14	VSS_208	T16
B11	VSS_209	T17
B8	VSS_210	T19
B2	VSS_211	T2
B19	VSS_212	T29
B14	VSS_213	T3
B11	VSS_214	T11
B8	VSS_215	T12
B2	VSS_216	T13
B19	VSS_217	T14
B14	VSS_218	T15
B11	VSS_219	T16
B8	VSS_220	T17
B2	VSS_221	T19
B19	VSS_222	T2
B14	VSS_223	T29
B11	VSS_224	T3
B8	VSS_225	T11
B2	VSS_226	T12
B19	VSS_227	T13
B14	VSS_228	T14
B11	VSS_229	T15
B8	VSS_230	T16
B2	VSS_231	T17
B19	VSS_232	T19
B14	VSS_233	T2
B11	VSS_234	T29
B8	VSS_235	T3
B2	VSS_236	T11
B19	VSS_237	T12
B14	VSS_238	T13
B11	VSS_239	T14
B8	VSS_240	T15
B2	VSS_241	T16
B19	VSS_242	T17
B14	VSS_243	T19
B11	VSS_244	T2
B8	VSS_245	T29
B2	VSS_246	T3
B19	VSS_247	T11
B14	VSS_248	T12
B11	VSS_249	T13
B8	VSS_250	T14
B2	VSS_251	T15
B19	VSS_252	T16
B14	VSS_253	T17
B11	VSS_254	T19
B8	VSS_255	T2
B2	VSS_256	T29
B19	VSS_257	T3
B14	VSS_258	T11
B11	VSS_259	T12
B8	VSS_260	T13
B2	VSS_261	T14
B19	VSS_262	T15
B14	VSS_263	T16
B11	VSS_264	T17
B8	VSS_265	T19
B2	VSS_266	T2
B19	VSS_267	T29
B14	VSS_268	T3
B11	VSS_269	T11
B8	VSS_270	T12
B2	VSS_271	T13
B19	VSS_272	T14
B14	VSS_273	T15
B11	VSS_274	T16
B8	VSS_275	T17
B2	VSS_276	T19
B19	VSS_277	T2
B14	VSS_278	T29
B11	VSS_279	T3
B8	VSS_280	T11
B2	VSS_281	T12
B19	VSS_282	T13
B14	VSS_283	T14
B11	VSS_284	T15
B8	VSS_285	T16
B2	VSS_286	T17
B19	VSS_287	T19
B14	VSS_288	T2
B11	VSS_289	T29
B8	VSS_290	T3
B2	VSS_291	T11
B19	VSS_292	T12
B14	VSS_293	T13
B11	VSS_294	T14
B8	VSS_295	T15
B2	VSS_296	T16
B19	VSS_297	T17
B14	VSS_298	T19
B11	VSS_299	T2
B8	VSS_300	T29
B2	VSS_301	T3

6 OF 6



FOXCONN PCEG

Title ICH10 -4	
Size C	Document Number TBGA01
Date Friday, August 22, 2008	Rev C1
Sheet 23 of 50	



5,20,24,27,28,30,32,36,44,46,47 SMB_CLK_RESUME
5,20,24,27,28,30,32,36,44,46,47 SMB_DATA_RESUME

20,24,27,30,32 WAKEJ
WAKEJ

All AC Coupling caps. should be placed within 250 mils of the connector

12 X_3X16_TXP0 XC60 0.1uF X_3X16_TXP0 C
12 X_3X16_TXN0 XC55 0.1uF X_3X16_TXN0 C
16V, X7R, +/-10%

12 X_3X16_TXP1 XC58 0.1uF X_3X16_TXP1 C
12 X_3X16_TXN1 XC61 0.1uF X_3X16_TXN1 C
16V, X7R, +/-10%

12 X_3X16_TXP2 XC63 0.1uF X_3X16_TXP2 C
12 X_3X16_TXN2 XC64 0.1uF X_3X16_TXN2 C
16V, X7R, +/-10%

12 X_3X16_TXP3 XC67 0.1uF X_3X16_TXP3 C
12 X_3X16_TXN3 XC68 0.1uF X_3X16_TXN3 C
16V, X7R, +/-10%

12 X_3X16_TXP4 XC71 0.1uF X_3X16_TXP4 C
12 X_3X16_TXN4 XC72 0.1uF X_3X16_TXN4 C
16V, X7R, +/-10%

12 X_3X16_TXP5 XC75 0.1uF X_3X16_TXP5 C
12 X_3X16_TXN5 XC76 0.1uF X_3X16_TXN5 C
16V, X7R, +/-10%

12 X_3X16_TXP6 XC79 0.1uF X_3X16_TXP6 C
12 X_3X16_TXN6 XC80 0.1uF X_3X16_TXN6 C
16V, X7R, +/-10%

12 X_3X16_TXP7 XC83 0.1uF X_3X16_TXP7 C
12 X_3X16_TXN7 XC84 0.1uF X_3X16_TXN7 C
16V, X7R, +/-10%

26 X_3_3X16_TXP8 XC87 0.1uF X_3_3X16_TXP8 C
26 X_3_3X16_TXN8 XC88 0.1uF X_3_3X16_TXN8 C
16V, X7R, +/-10%

26 X_3_3X16_TXP9 XC89 0.1uF X_3_3X16_TXP9 C
26 X_3_3X16_TXN9 XC90 0.1uF X_3_3X16_TXN9 C
16V, X7R, +/-10%

26 X_3_3X16_TXP10 XC91 0.1uF X_3_3X16_TXP10 C
26 X_3_3X16_TXN10 XC92 0.1uF X_3_3X16_TXN10 C
16V, X7R, +/-10%

26 X_3_3X16_TXP11 XC93 0.1uF X_3_3X16_TXP11 C
26 X_3_3X16_TXN11 XC94 0.1uF X_3_3X16_TXN11 C
16V, X7R, +/-10%

26 X_3_3X16_TXP12 XC95 0.1uF X_3_3X16_TXP12 C
26 X_3_3X16_TXN12 XC96 0.1uF X_3_3X16_TXN12 C
16V, X7R, +/-10%

26 X_3_3X16_TXP13 XC97 0.1uF X_3_3X16_TXP13 C
26 X_3_3X16_TXN13 XC98 0.1uF X_3_3X16_TXN13 C
16V, X7R, +/-10%

26 X_3_3X16_TXP14 XC99 0.1uF X_3_3X16_TXP14 C
26 X_3_3X16_TXN14 XC100 0.1uF X_3_3X16_TXN14 C
16V, X7R, +/-10%

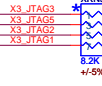
26 X_3_3X16_TXP15 XC101 0.1uF X_3_3X16_TXP15 C
26 X_3_3X16_TXN15 XC102 0.1uF X_3_3X16_TXN15 C
16V, X7R, +/-10%

PCI-E2_16X

KEY

Slot-PCI-E16X

28038211-020-4P



5,20,24,27,28,30,32,36,44,46,47 SMB_CLK_RESUME
5,20,24,27,28,30,32,36,44,46,47 SMB_DATA_RESUME

20,24,27,30,32 WAKEJ
WAKEJ

All AC Coupling caps. should be placed within 250 mils of the connector

26 X_4_3X16_TXP8 XC56 0.1uF X_4_3X16_TXP8 C
26 X_4_3X16_TXN8 XC57 0.1uF X_4_3X16_TXN8 C
16V, X7R, +/-10%

26 X_4_3X16_TXP9 XC59 0.1uF X_4_3X16_TXP9 C
26 X_4_3X16_TXN9 XC62 0.1uF X_4_3X16_TXN9 C
16V, X7R, +/-10%

26 X_4_3X16_TXP10 XC65 0.1uF X_4_3X16_TXP10 C
26 X_4_3X16_TXN10 XC66 0.1uF X_4_3X16_TXN10 C
16V, X7R, +/-10%

26 X_4_3X16_TXP11 XC69 0.1uF X_4_3X16_TXP11 C
26 X_4_3X16_TXN11 XC70 0.1uF X_4_3X16_TXN11 C
16V, X7R, +/-10%

26 X_4_3X16_TXP12 XC73 0.1uF X_4_3X16_TXP12 C
26 X_4_3X16_TXN12 XC74 0.1uF X_4_3X16_TXN12 C
16V, X7R, +/-10%

26 X_4_3X16_TXP13 XC77 0.1uF X_4_3X16_TXP13 C
26 X_4_3X16_TXN13 XC78 0.1uF X_4_3X16_TXN13 C
16V, X7R, +/-10%

26 X_4_3X16_TXP14 XC81 0.1uF X_4_3X16_TXP14 C
26 X_4_3X16_TXN14 XC82 0.1uF X_4_3X16_TXN14 C
16V, X7R, +/-10%

26 X_4_3X16_TXP15 XC85 0.1uF X_4_3X16_TXP15 C
26 X_4_3X16_TXN15 XC86 0.1uF X_4_3X16_TXN15 C
16V, X7R, +/-10%

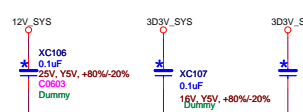
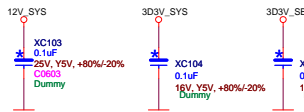
11,24 PCIe8_PSRNT

PCI-E2_8X

KEY

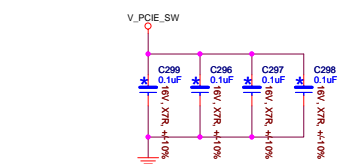
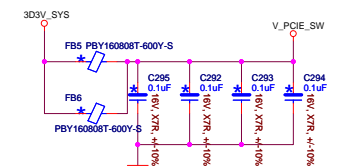
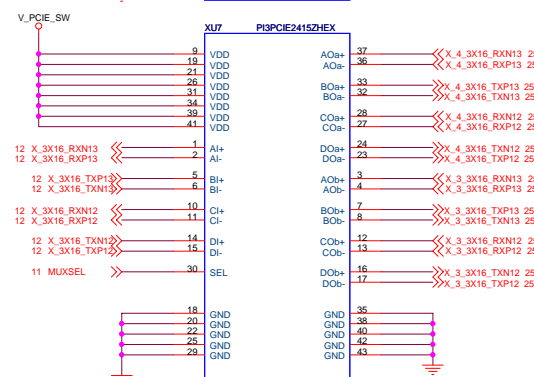
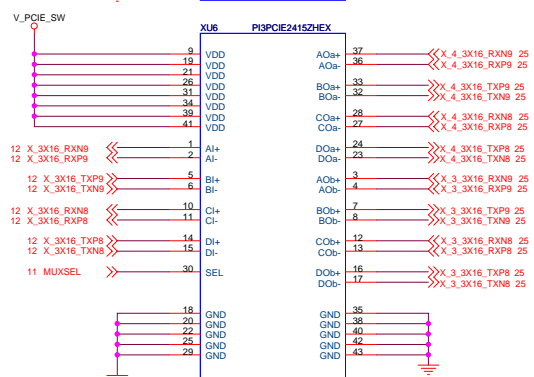
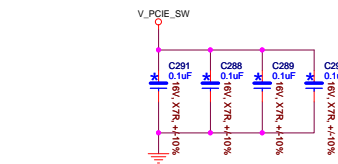
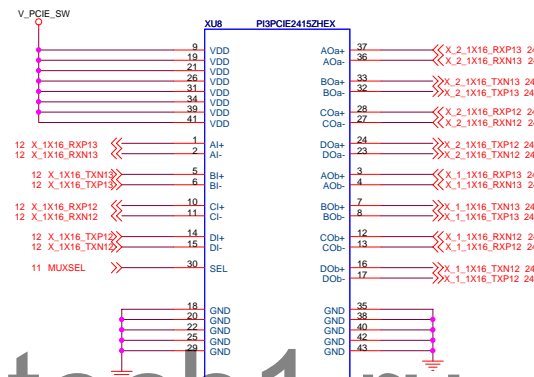
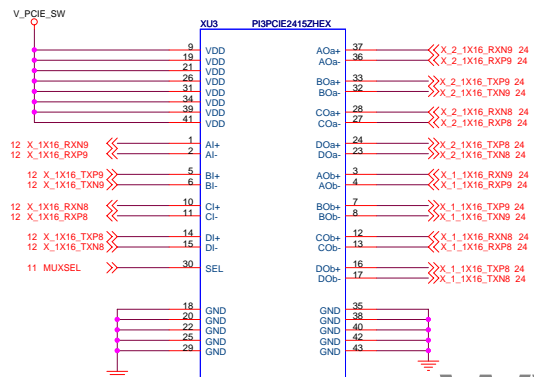
Slot-PCI-E16X
pci164_x16n150

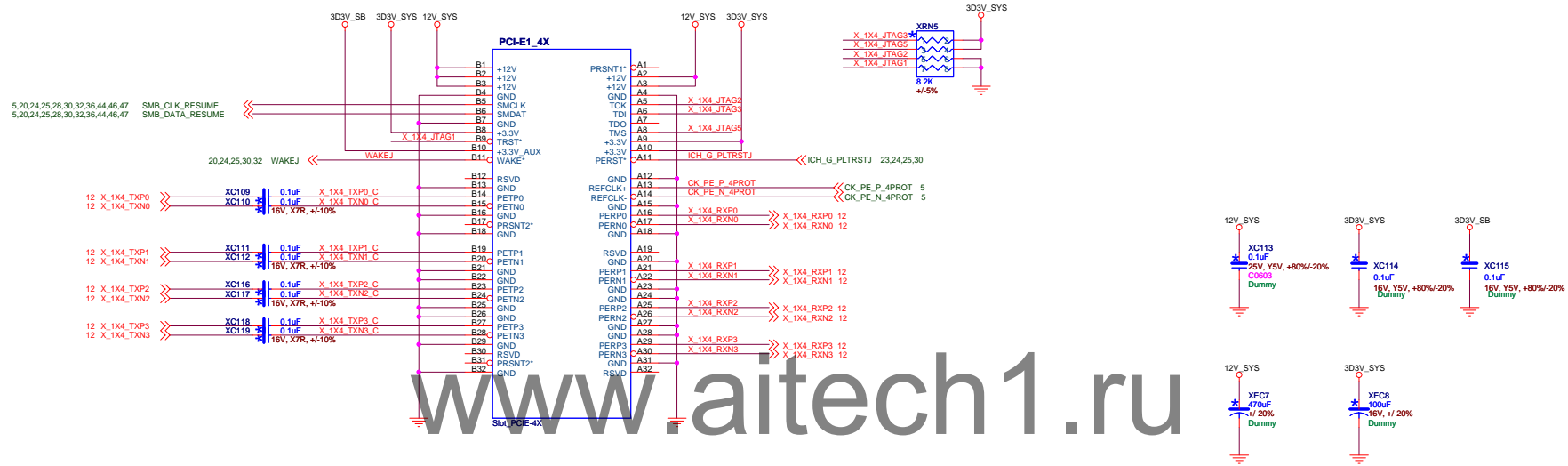
28038211-020-4P



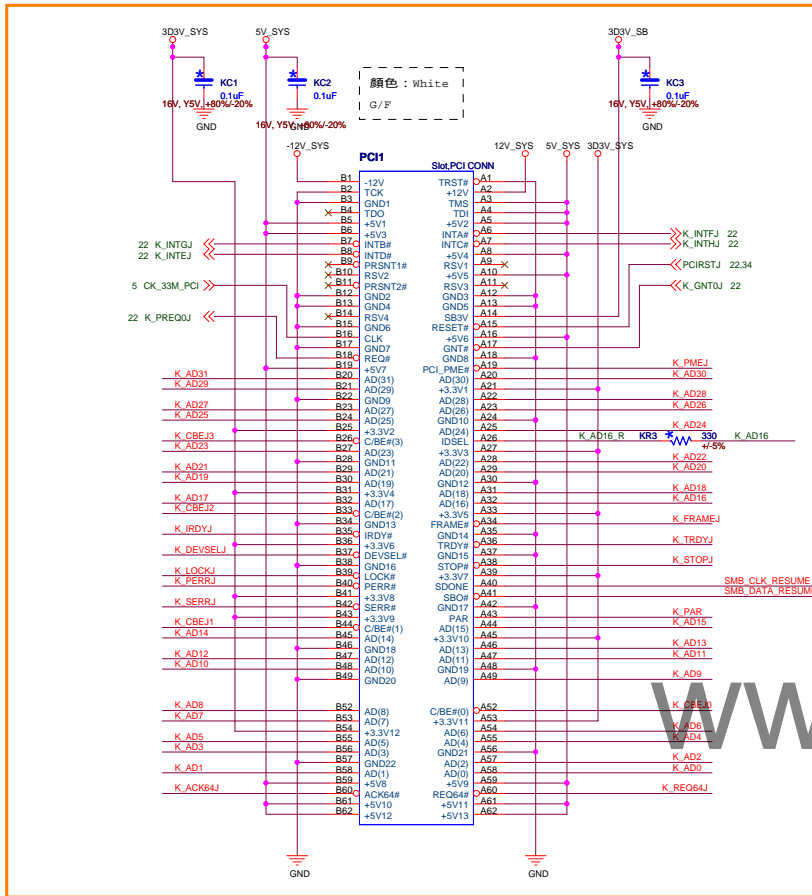
FOXCONN PCEG

File			
PCIE X16 SLOT 2.3			
Standard Circuit			
Size	Document Number	Rev	C1
C			
Date:	Tuesday, August 18, 2008	Sheet	25 of 50

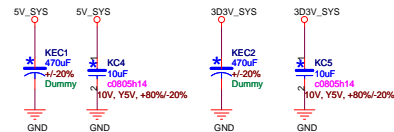
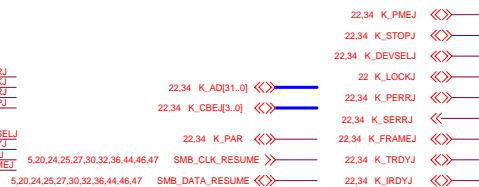
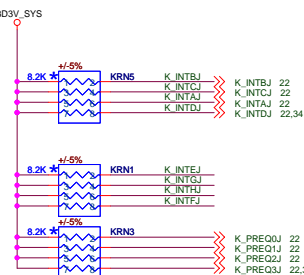
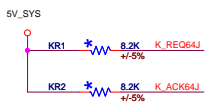


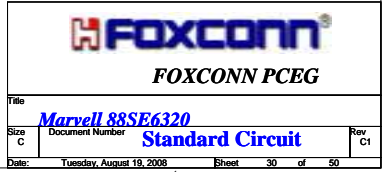


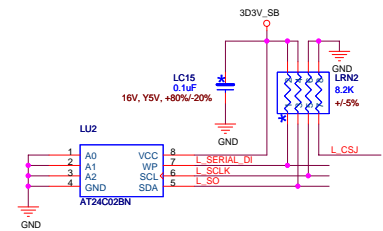
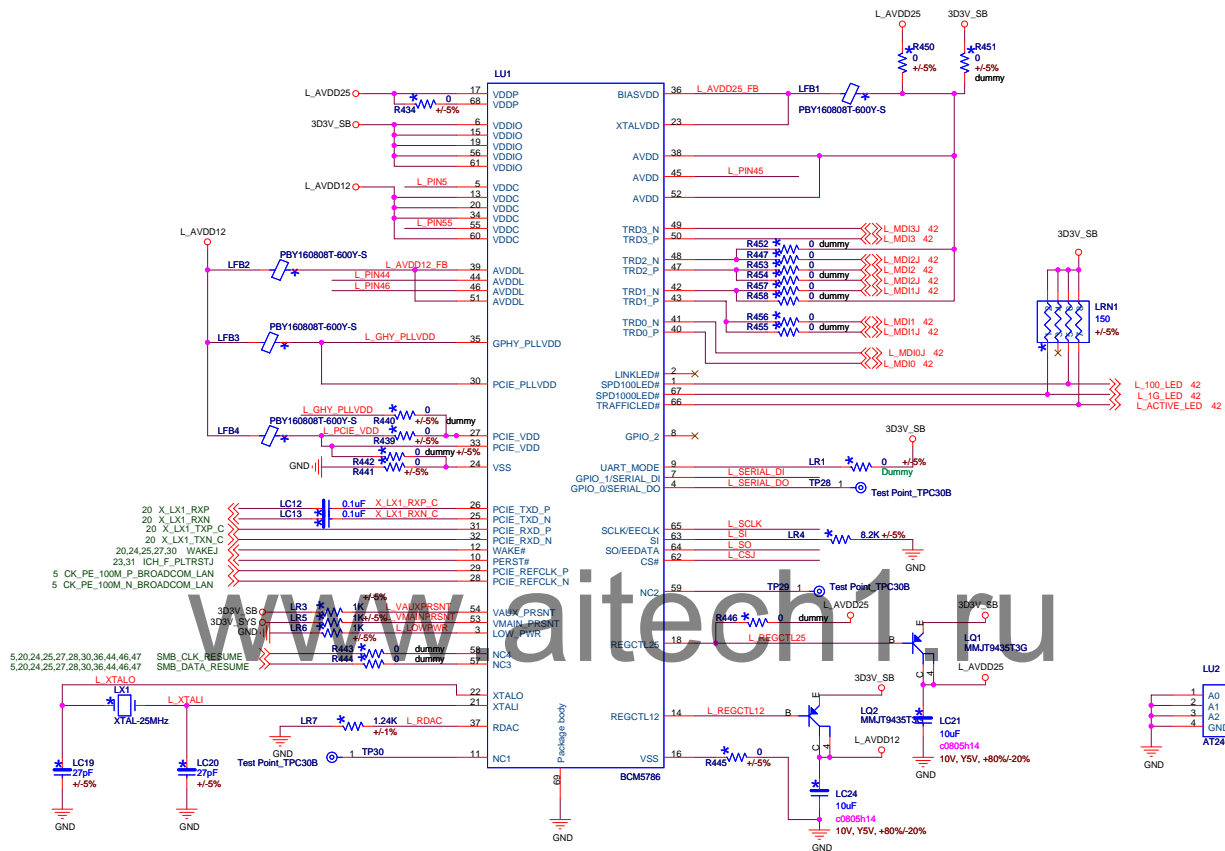
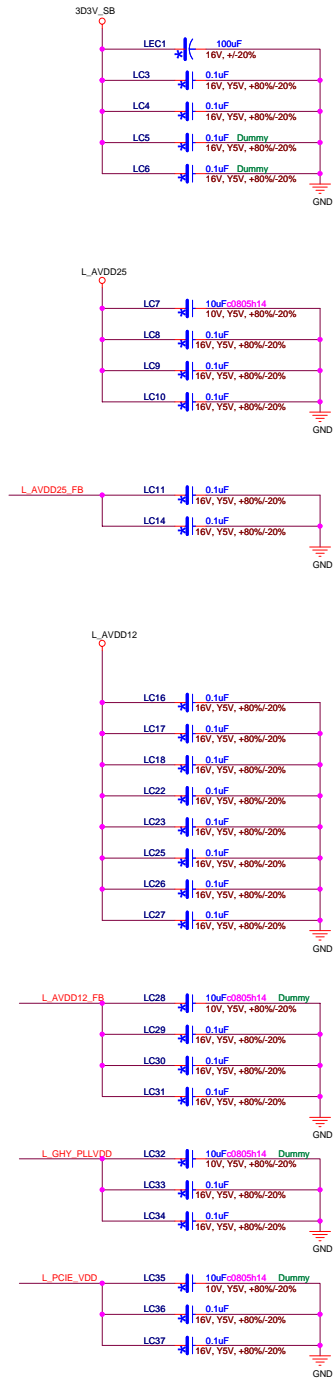
www.aitech1.ru



www.aitech1.ru






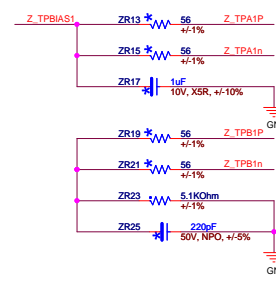
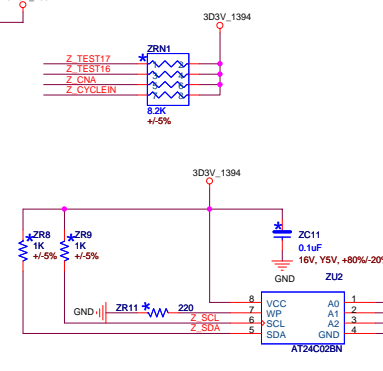
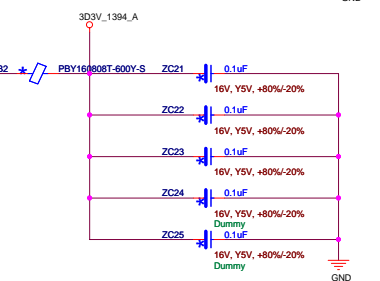


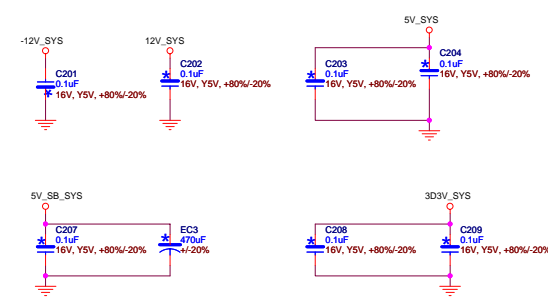
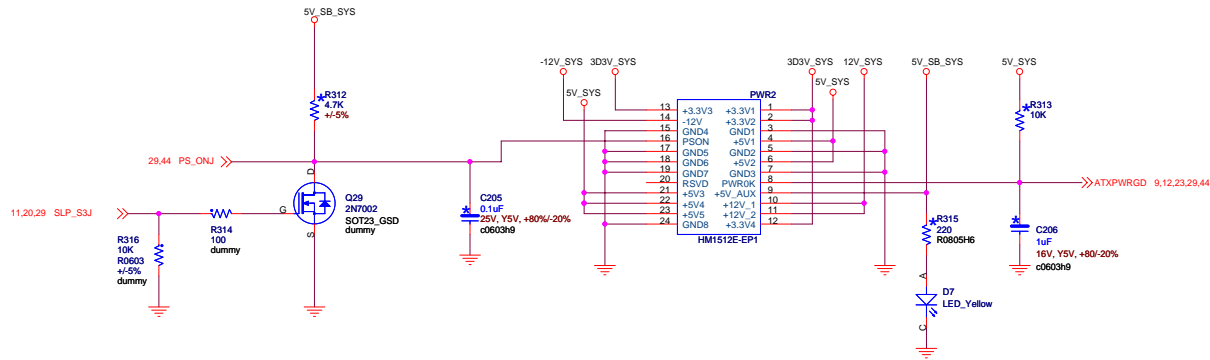
FOXCONN PCEG

Title				
LAN Boardcom BCM5786				
Size	Document Number			Rev
C	Standard Circuit			C1
Date:	Tuesday, August 19, 2008	Sheet	32	of 50

www.aitech1.ru

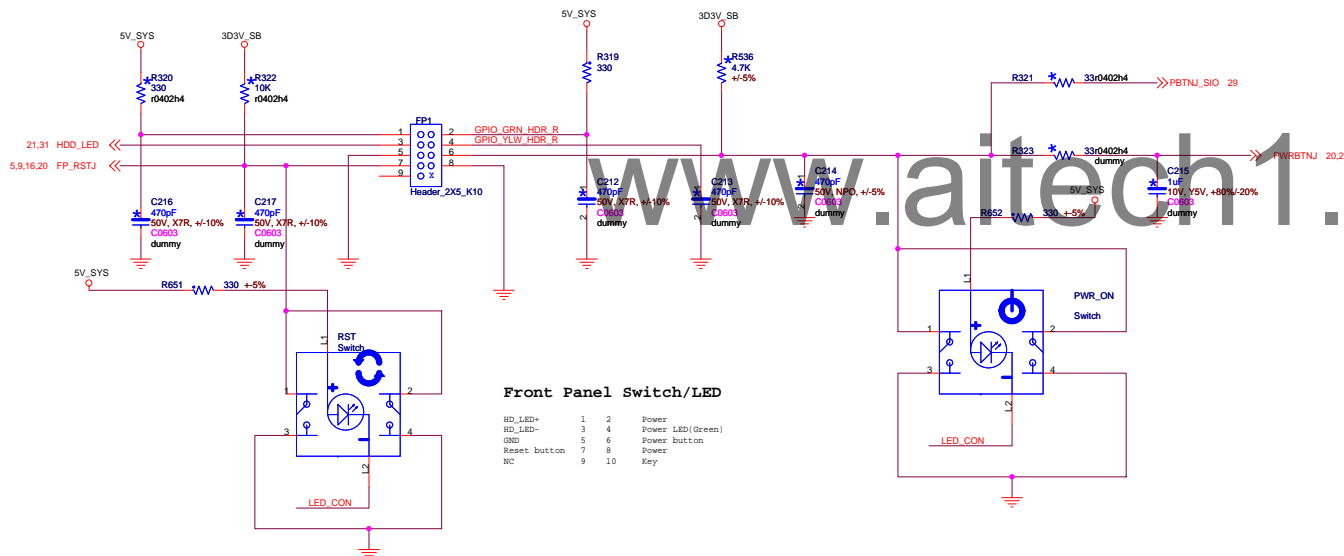
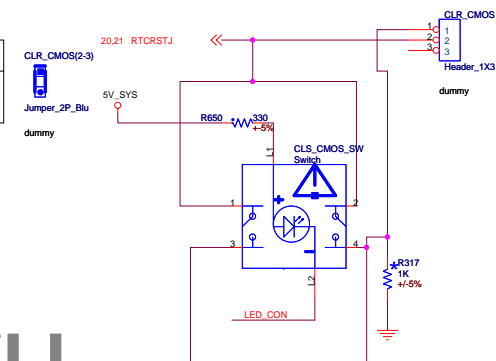
 FOXCONN PCEG		
Title		
LAN Boardcom BCM5786		
Size	Document Number	Rev
C	Standard Circuit	C1
Date:	Tuesday, August 18, 2008	Sheet 33 of 50





Clear CMOS

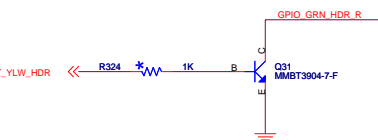
CLR_CMOS	CMOS
Clear	(1-2)
Normal	(2-3)



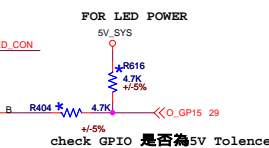
Front Panel Switch/LED

HD_LED+	1	2	Power
HD_LED-	3	4	Power LED(Green)
GND	5	6	Power button
Reset button	7	8	Power
NC	9	10	Key

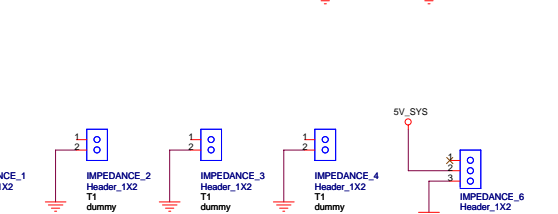
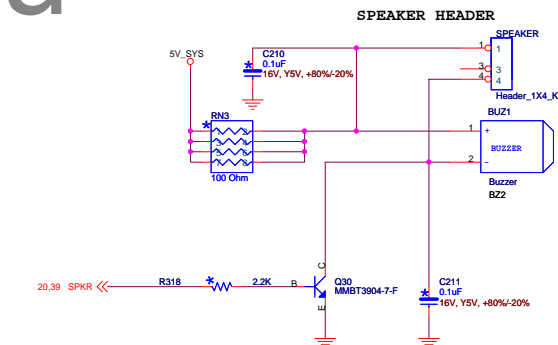
Chassis Intruder Header



S0 : Power LED is on;
S1 : Power LED is blinking;
S3~S5: Power LED is off.



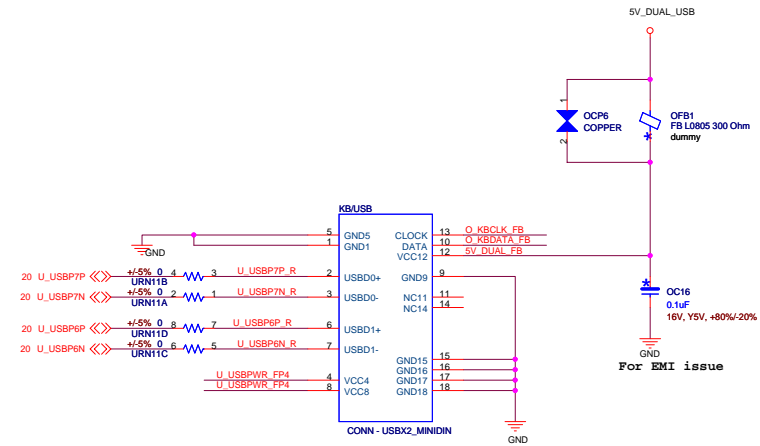
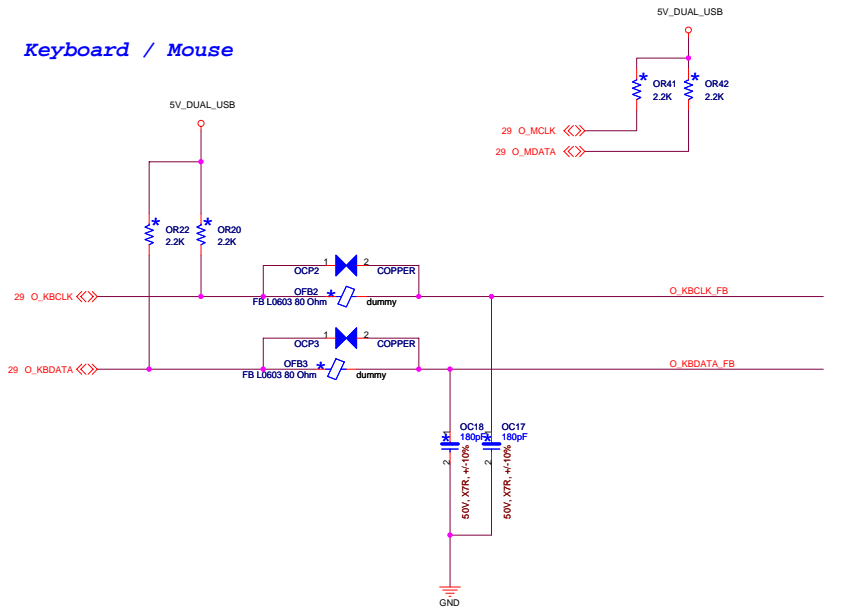
check GPIO 是否為5V Tolerance



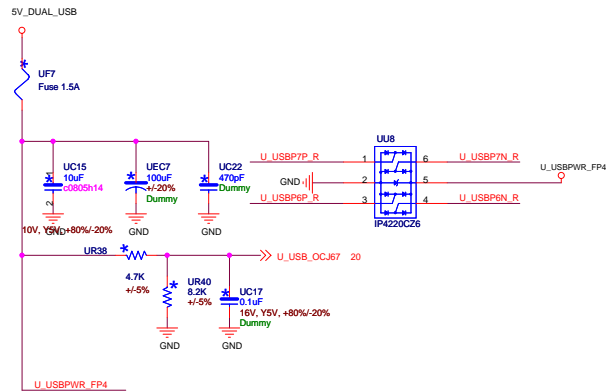
FOXCONN PCEG

File		
Power / MISC Connectors		
Size	Document Number	Rev
C	TBGA01	C1
Date:	Tuesday, August 18, 2008	Sheet 35 of 50

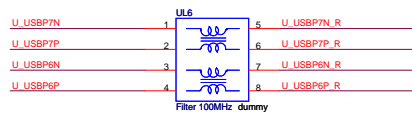
Keyboard / Mouse

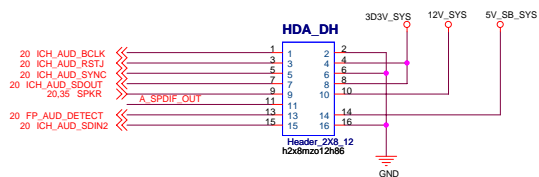


www.aitech1.ru

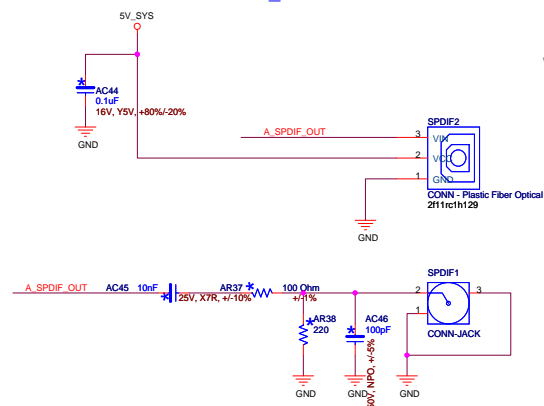


USB Front Header 4





SPDIF_OUT



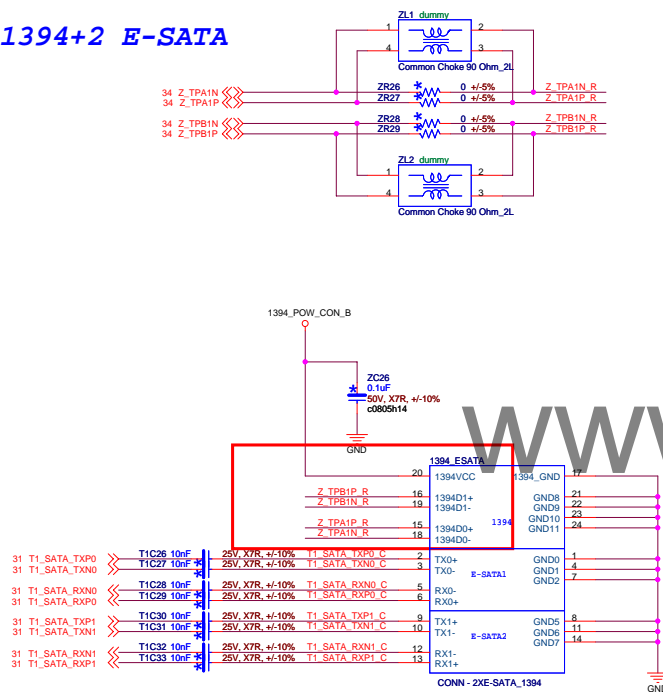
www.aitech1.ru

FOXCONN

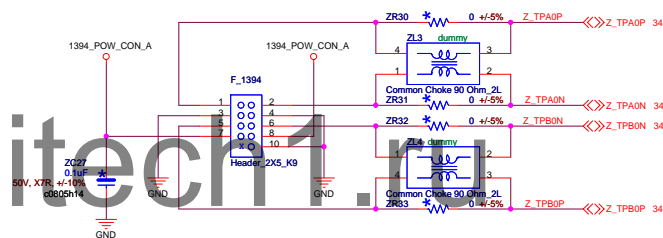
FOXCONN PCEG

Title		
HDA Connectors		
Size	Document Number	Rev
C		C1
Date:	Tuesday, August 18, 2008	Sheet 39 of 50

Real_1394+2 E-SATA



Front_1394



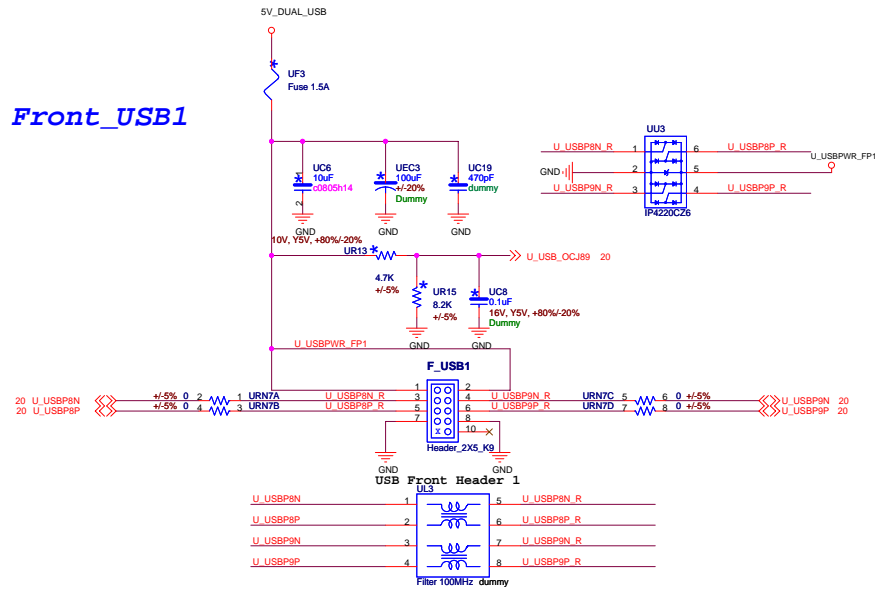
FOXCONN PCEG

Title **Connector**

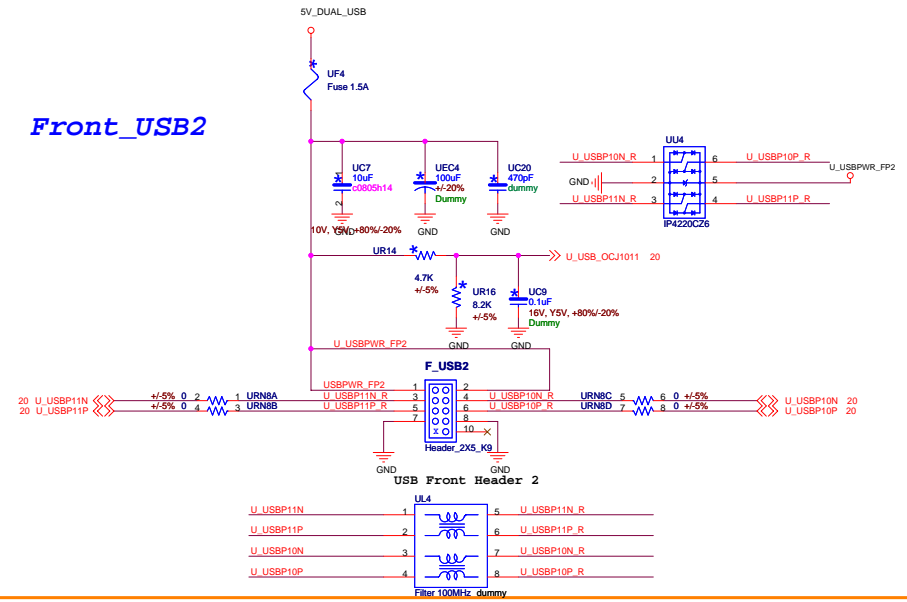
Size C Document Number **Standard Circuit**

Date: Tuesday, August 18, 2008 Sheet 40 of 50

Front_USB1



Front_USB2



www.aitech1.ru

FOXCONN

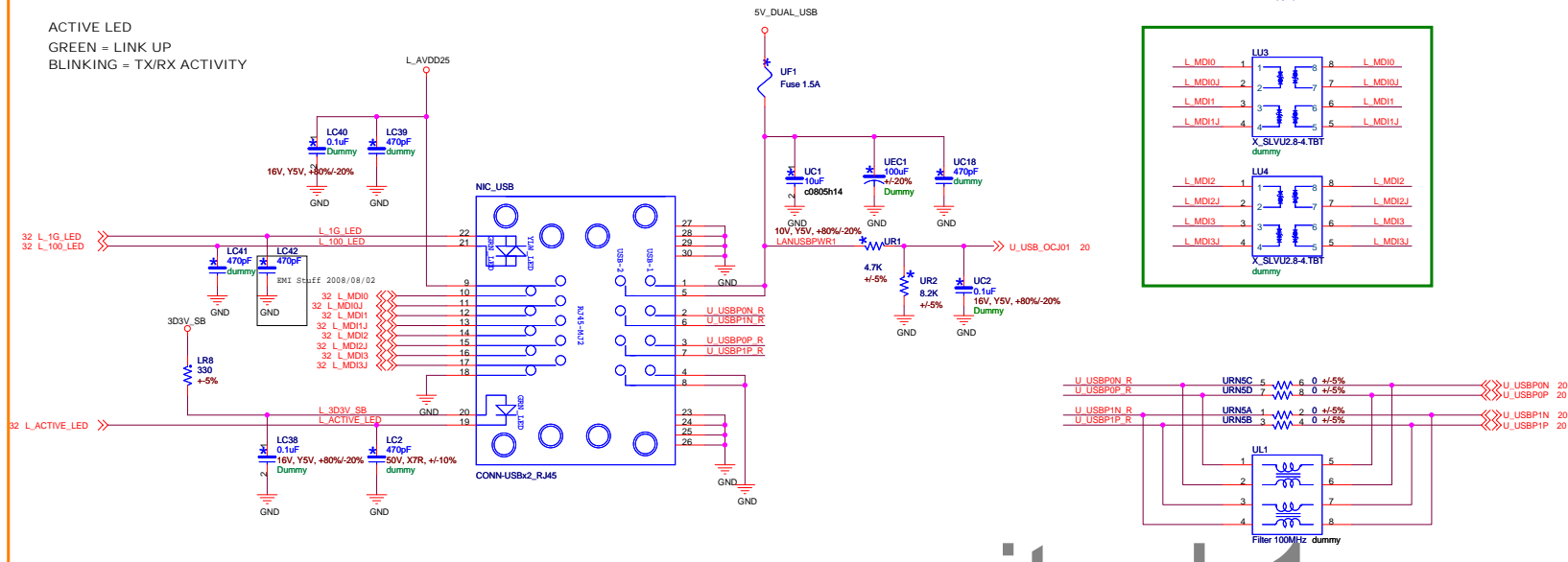
FOXCONN PCEG

Title		Front USB Connector
Size	Document Number	Standard Circuit
C		
Date:	Tuesday, August 18, 2008	Sheet 41 of 50

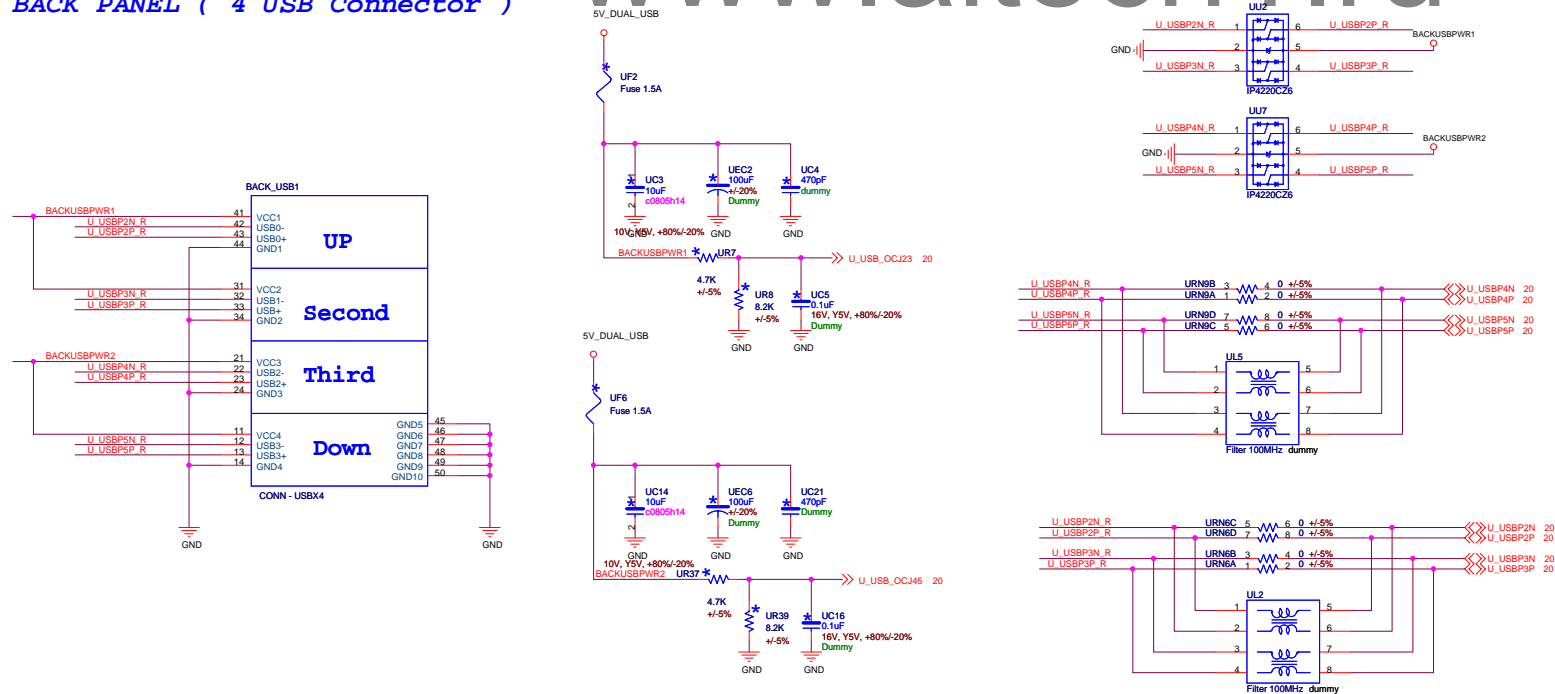
SPEED LED	
LINK 10M	OFF
LINK 100M	GREEN
LINK 1000M	YELLOW

BACK PANEL (LAN 1+ 2 USB Connector)


ACTIVE LED
GREEN = LINK UP
BLINKING = TX/RX ACTIVITY

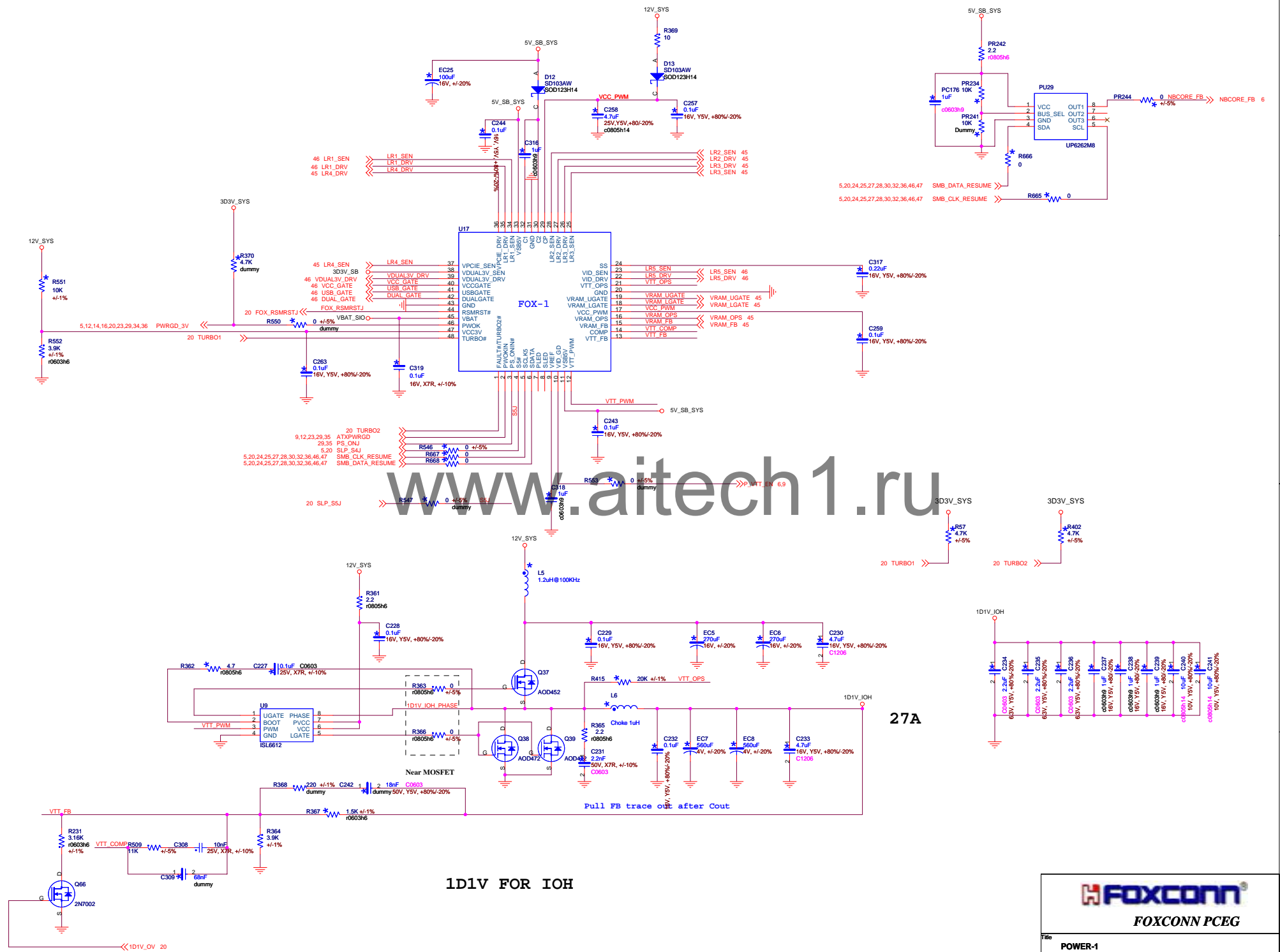


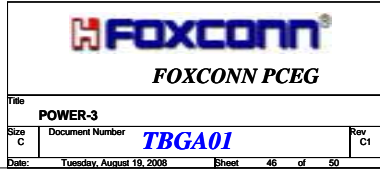
BACK PANEL (4 USB Connector)



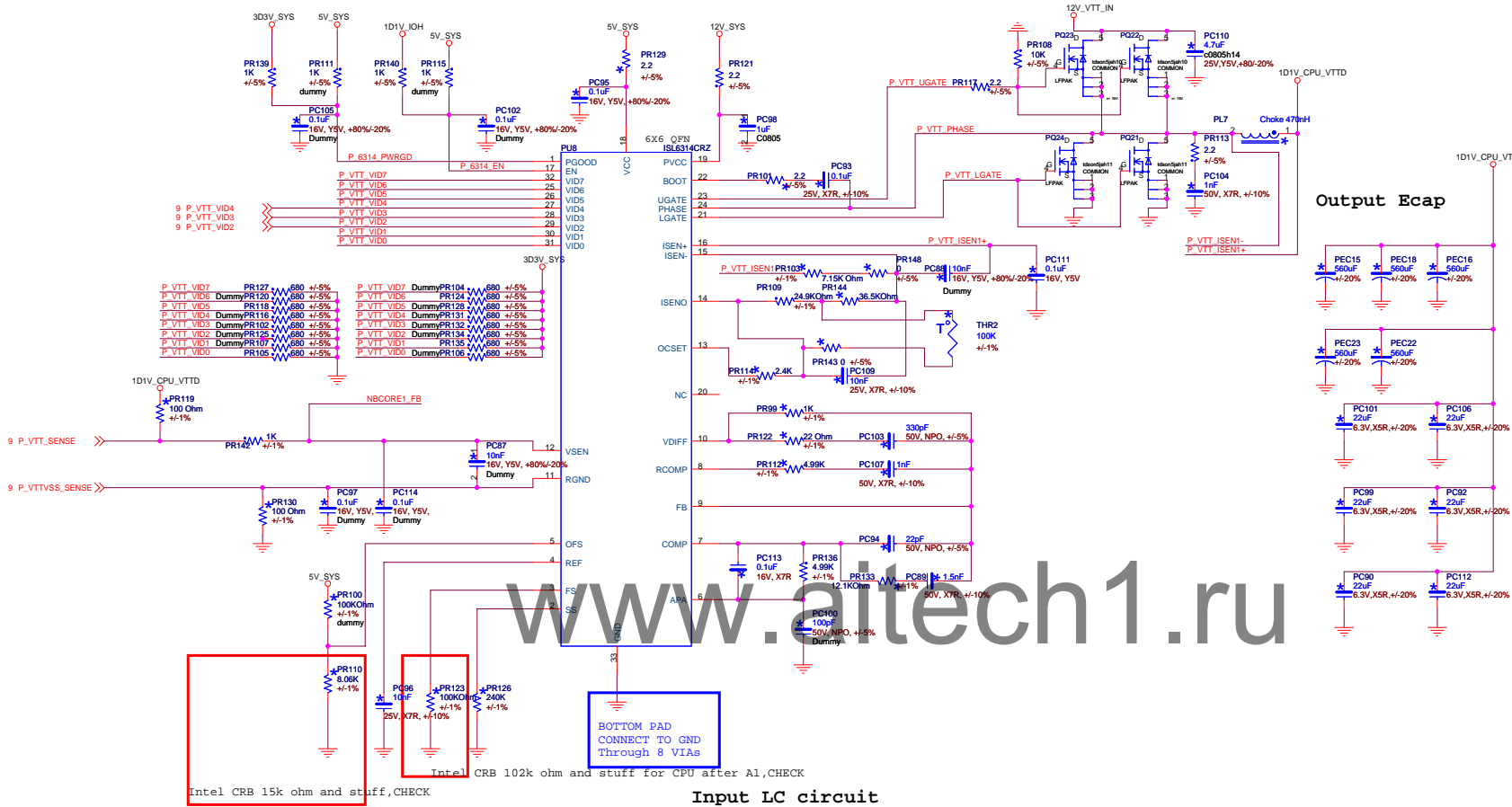
www.aitech1.ru

 FOXCONN PCEG		
Title		
FOX-DOLL		
Size	Document Number	Rev
C	Standard Circuit	C1
Date:	Tuesday, August 18, 2008	Sheet 43 of 50

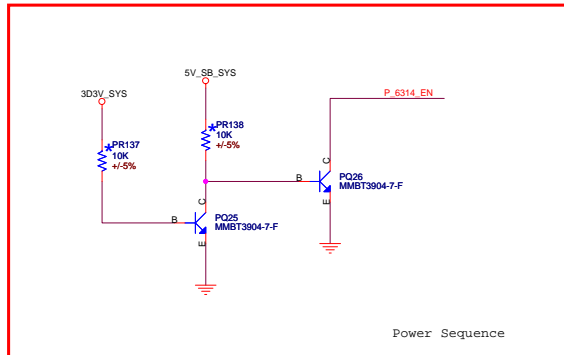
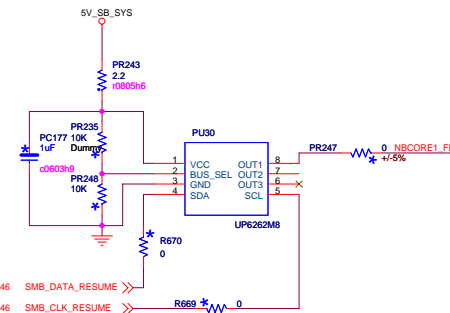
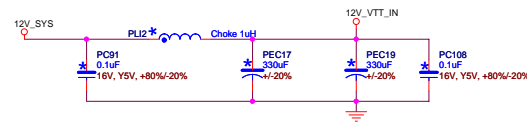




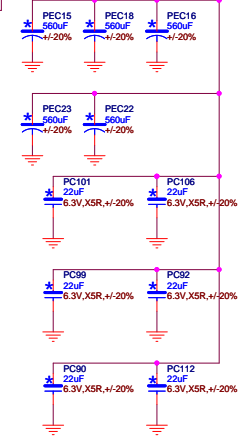
ISL6314CR POWER



Input LC circuit

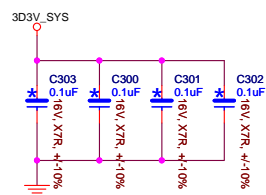


Output Ecap

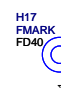
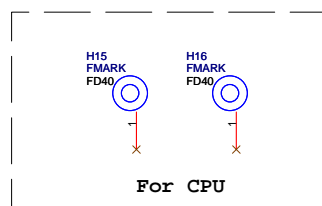
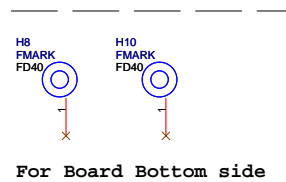
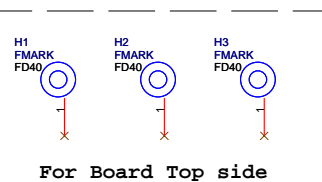
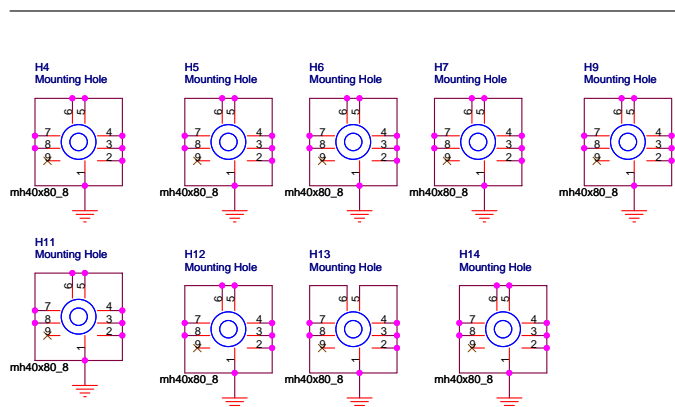


FOXCONN PCEG

Title		
POWER-4		
Size	Document Number	Rev
C	TBGA01	C1
Date:	Friday, August 22, 2008	Sheet 47 of 50



www.aitech1.ru



Title			<Title>
Size	Document Number	Rev	
A3	<Doc>	<RevCode>	
Date:	Tuesday, August 19, 2008	Sheet	48 of 50


ICH10 GPIO Summary

Name	Power Well	Type	Description
GPIO0	3.3V	I/O	FP_AUD_DETECT
GPIO1	3.3V	I/O	Unused(pull-up)
GPIO2	5V	I/OD	PIRQE#
GPIO3	5V	I/OD	PIRQF#
GPIO4	5V	I/OD	PIRQG#
GPIO5	5V	I/OD	PIRQH#
GPIO6	3.3V	I/O	Unused(pull-up)
GPIO7	3.3V	I/O	Unused(pull-up)
GPIO8	3.3V_SB	I/O	CSI_FREQ_STRAP1
GPIO9	3.3V_SB	I/O	Unused(pull-down)
GPIO10	3.3V_SB	I/O	Unused(pull-down)
GPIO11	3.3V_SB	I/O	Unused(NC)
GPIO12	3.3V_SB	I/O	CSI_FREQ_STRAP0
GPIO13	3.3V_SB	I/O	L_PME#
GPIO14	3.3V_SB	I/O	Unused(pull-up)
GPIO15	3.3V_SB	I/O	Unused(NC)
GPIO16	3.3V	I/O	DRAM_OV
GPIO17	3.3V	I/O	Unused(pull-up)
GPIO18	3.3V	I/O	Unused(NC)
GPIO19	3.3V	I/O	Unused(pull-up)
GPIO20	3.3V	I/O	SLOW_MODE
GPIO21	3.3V	I/O	Unused(pull-up)
GPIO22	3.3V	I/O	Unused(pull-up)
GPIO23	3.3V	I/O	Unused(NC)
GPIO24	3.3V_SB	I/O	DRAM_OV
GPIO25	3.3V_SB	I/O	Unused(NC)
GPIO26	3.3V_SB	I/O	Unused(NC)
GPIO27	3.3V_SB	I/O	IOH_OV
GPIO28	3.3V_SB	I/O	QRT_YLW_HDR
GPIO29	3.3V_SB	I/O	U_USB_OCJ45
GPIO30	3.3V_SB	I/O	U_USB_OCJ67
GPIO31	3.3V_SB	I/O	U_USB_OCJ67
GPIO32	3.3V	I/O	TURBO1
GPIO33	3.3V	I/O	TURBO2
GPIO34	3.3V	I/O	DRAM_OV
GPIO35	3.3V	I/O	LOAD_CSI_PARAM
GPIO36	3.3V	I/O	Unused(pull-up)
GPIO37	3.3V	I/O	Unused(pull-up)
GPIO38	3.3V	I/O	Unused(pull-up)
GPIO39	3.3V	I/O	Unused(pull-down)
GPIO40	3.3V_SB	I/O	U_USB_OCJ01
GPIO41	3.3V_SB	I/O	U_USB_OCJ23
GPIO42	3.3V_SB	I/O	U_USB_OCJ23
GPIO43	3.3V_SB	I/O	U_USB_OCJ45
GPIO44	3.3V_SB	N/A	U_USB_OCJ89
GPIO45	3.3V_SB	N/A	U_USB_OCJ89
GPIO46	3.3V_SB	N/A	U_USB_OCJ1011
GPIO47	3.3V_SB	N/A	U_USB_OCJ1011
GPIO48	3.3V	I/O	Unused(pull-up)
GPIO49	3.3V	I/O	DMI_STRAP(pull-down)
GPIO50	5V	I/O	K_PREQ1J
GPIO51	3.3V	I/O	K_GNT1J
GPIO52	5V	I/O	K_PREQ2J
GPIO53	3.3V	I/O	Unused(NC)
GPIO54	5V	I/O	K_PREQ3J
GPIO55	3.3V	I/O	K_GNT3
GPIO56	3.3V_SB	I/O	IOH_CSI_RSTJ
GPIO57	3.3V_SB	I/O	Unused(pull-down)
GPIO58	3.3V_SB	I/O	Unused(NC)
GPIO59	3.3V_SB	I/O	U_USB_OCJ01
GPIO60	3.3V_SB	I/O	Unused(pull-up)

PCI Routing Summary

	PCI1	1394				
INTAJ	F					
INTBJ	G					
INTCJ	H					
INTDJ	E					
INTEJ						
INTFJ						
INTGJ						
INTHJ						
REG#/GNT#	0					
IDSEL	16	19				

www.aitech1.ru

 FOXCONN PCEG		
File		
GPIO / IRQ / IDSEL Map		
Size	Document Number	Rev
C	TBGA01	C1
Date:	Tuesday, August 18, 2008	Sheet 49 of 50

2007

11/27 FAB A Gerber Out

2008

1/16 Change PCIE SW to PI3PCIE2415

www.aitech1.ru



FOXCONN PCEG

History		
Title		
Size C	Document Number	Rev C1
	TBGA01	
Date:	Tuesday, August 19, 2008	Sheet 50 of 50